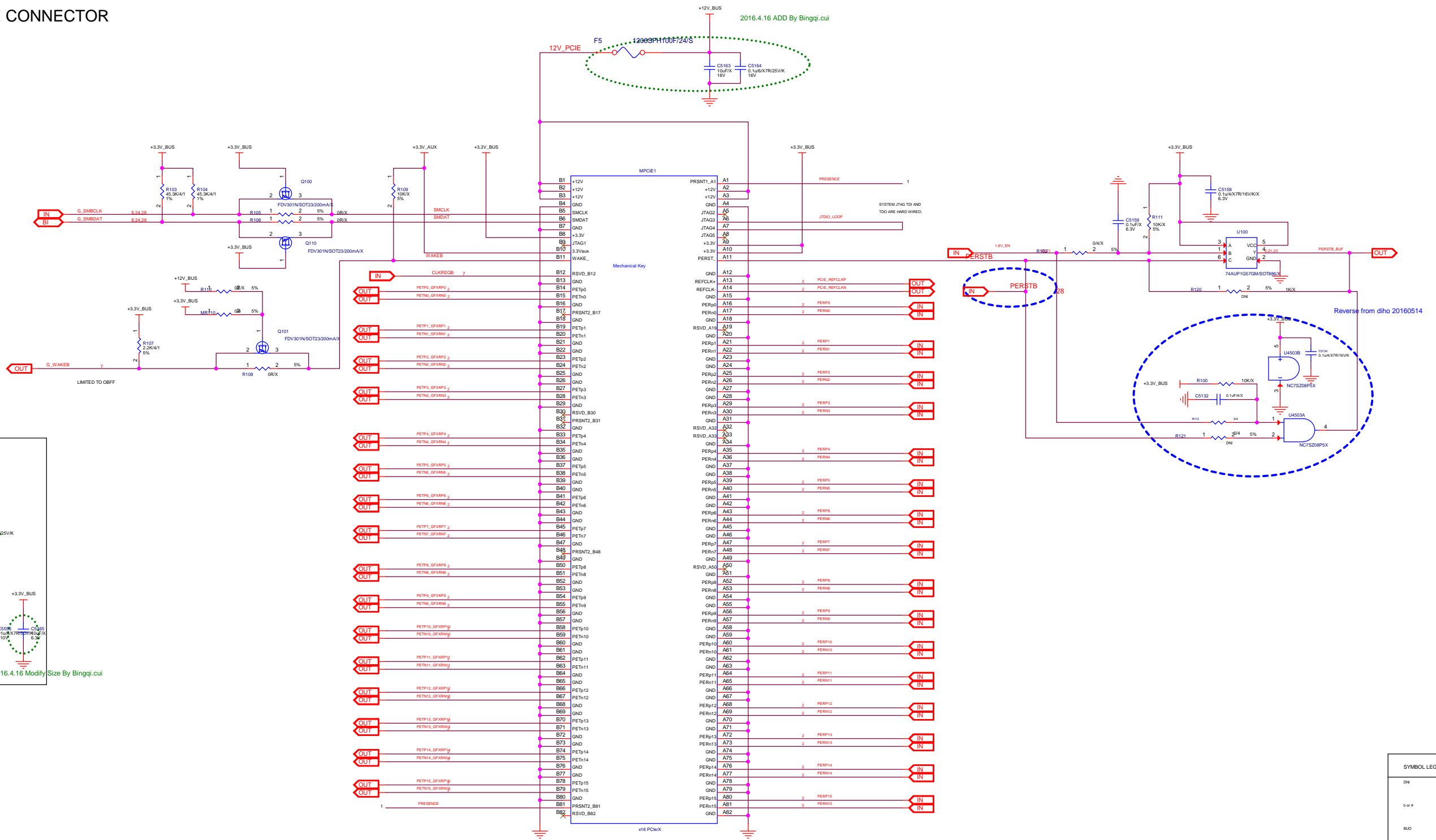


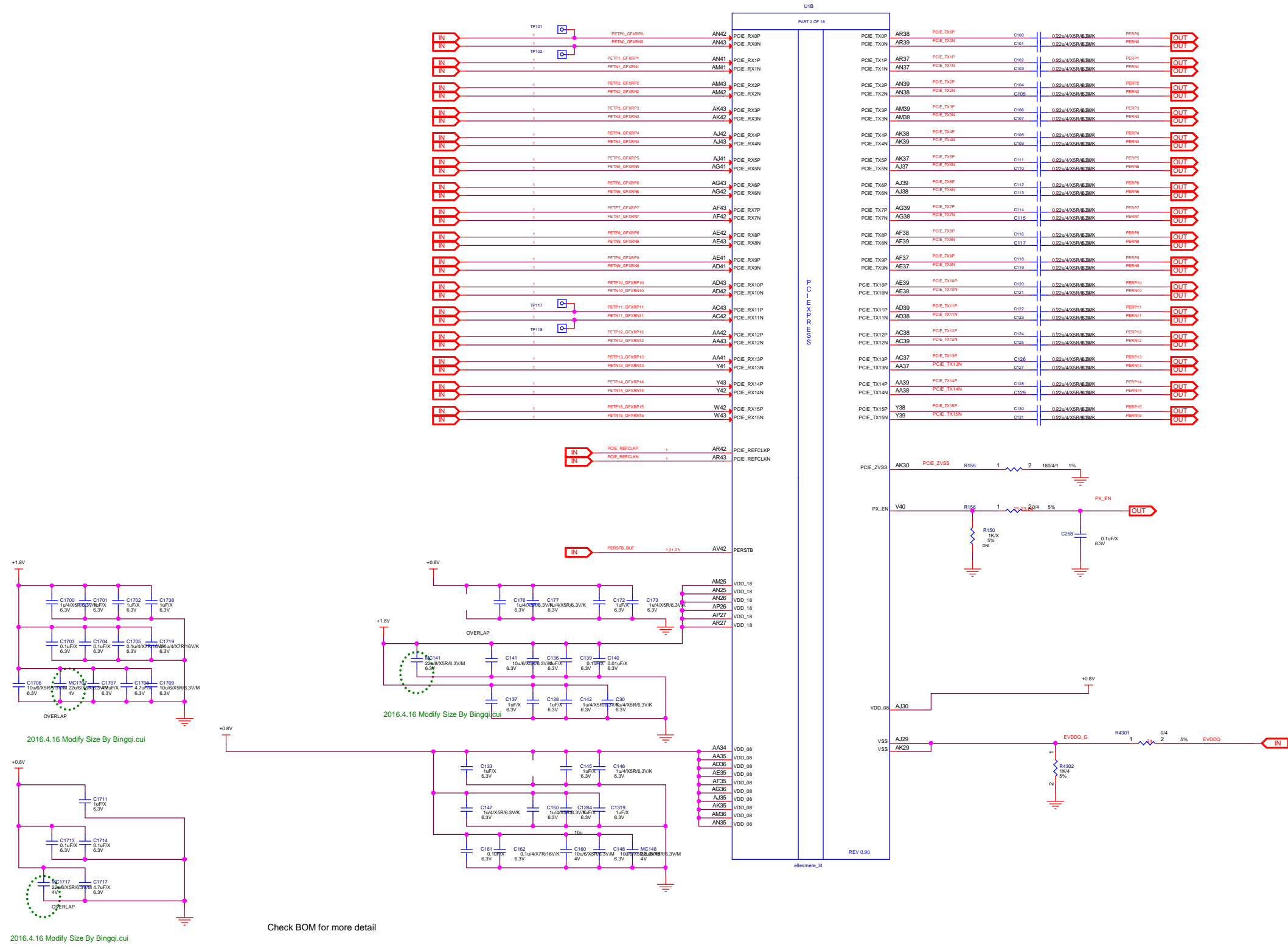


(1) PCI-EXPRESS EDGE CONNECTOR

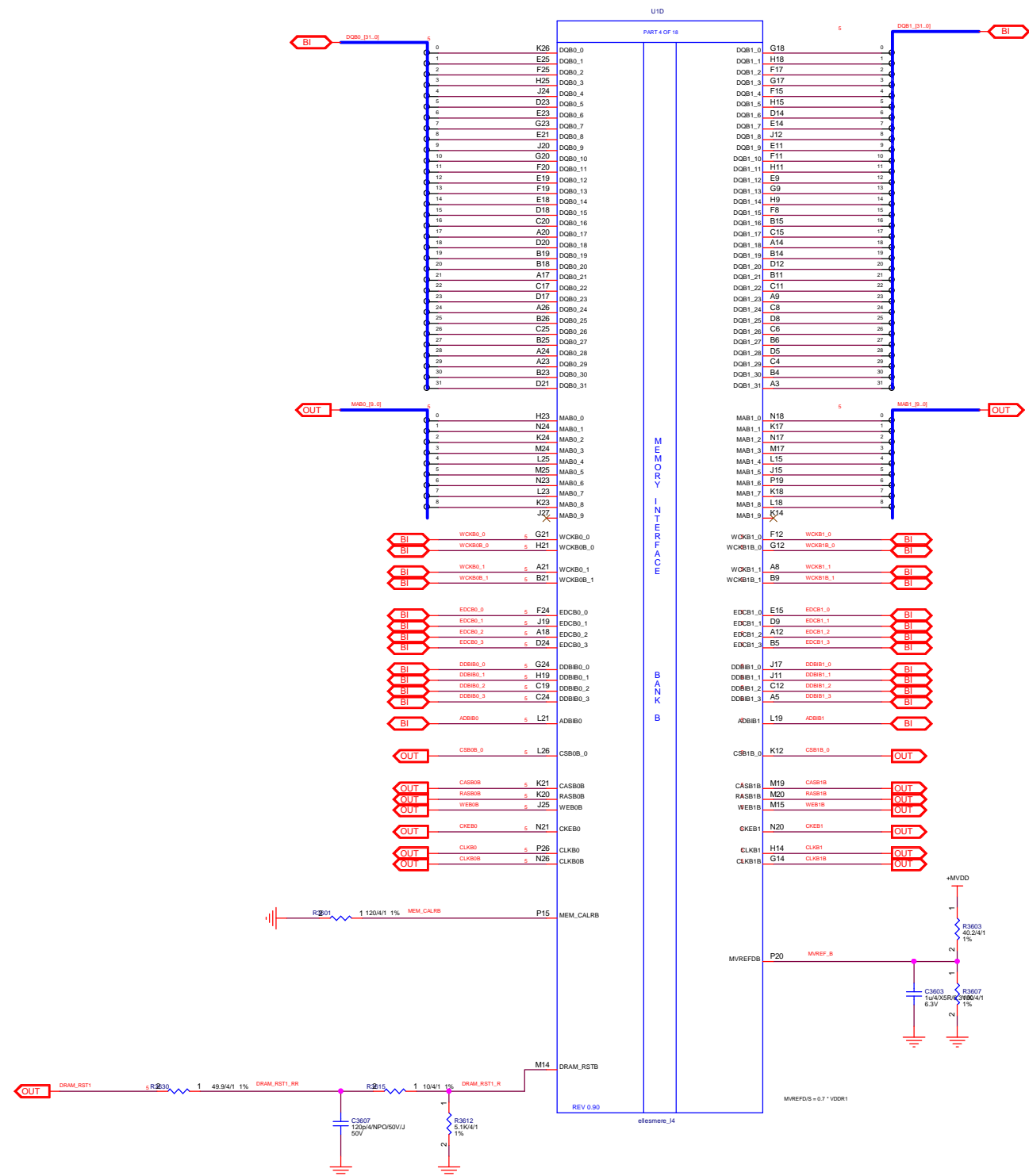
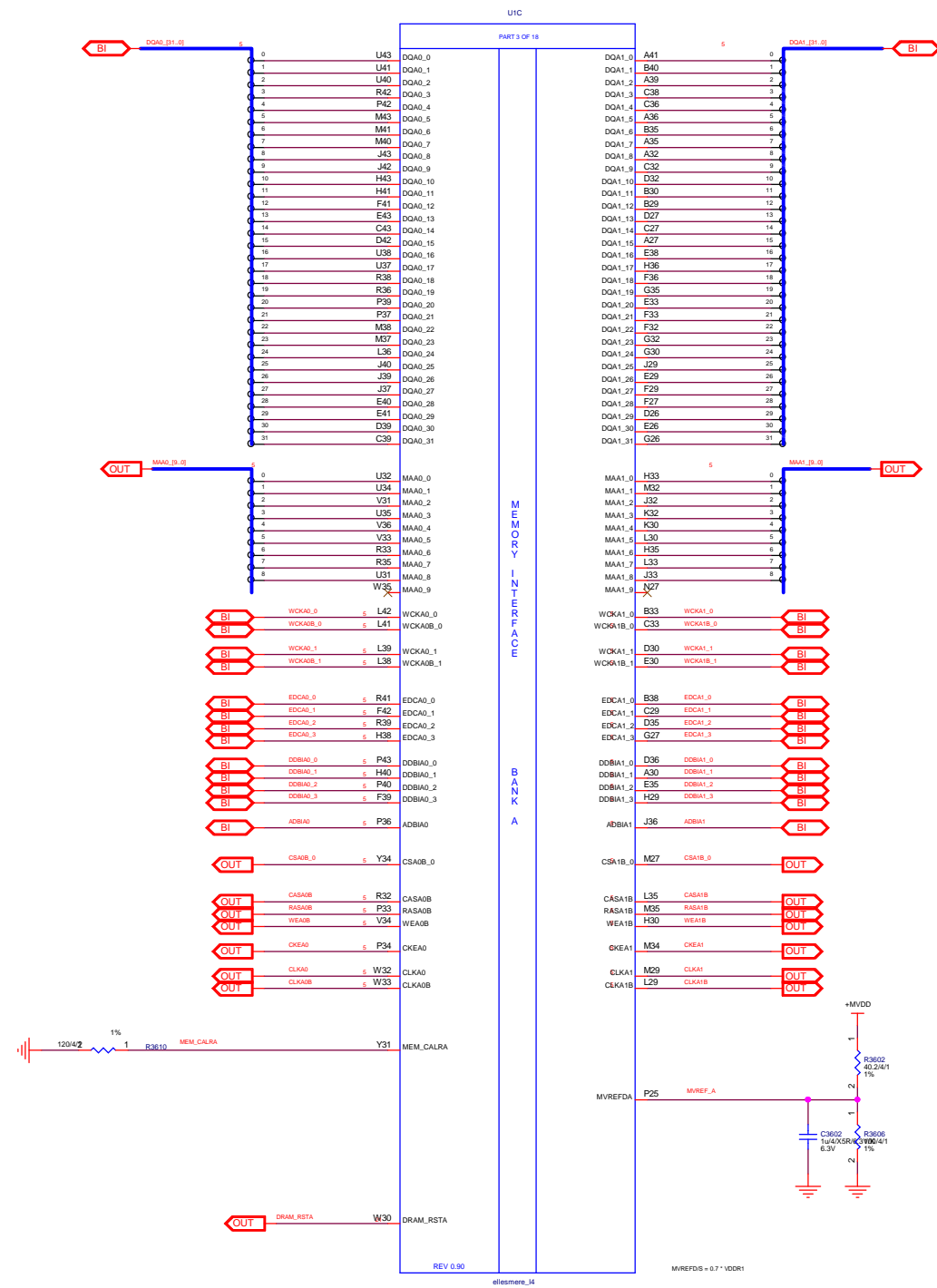


SYMBOL LEGEND	
DN	DO NOT INSTALL
B or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

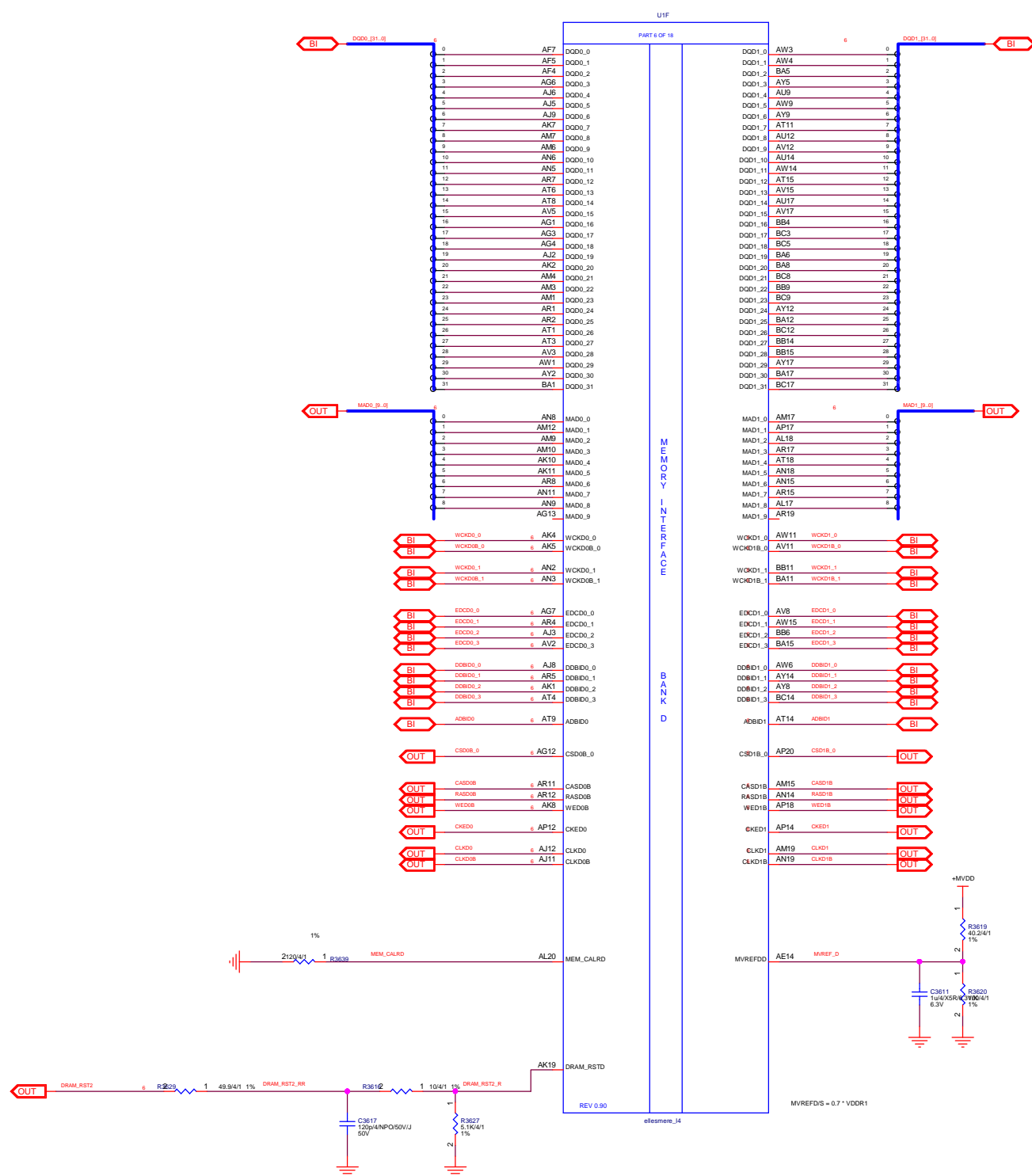
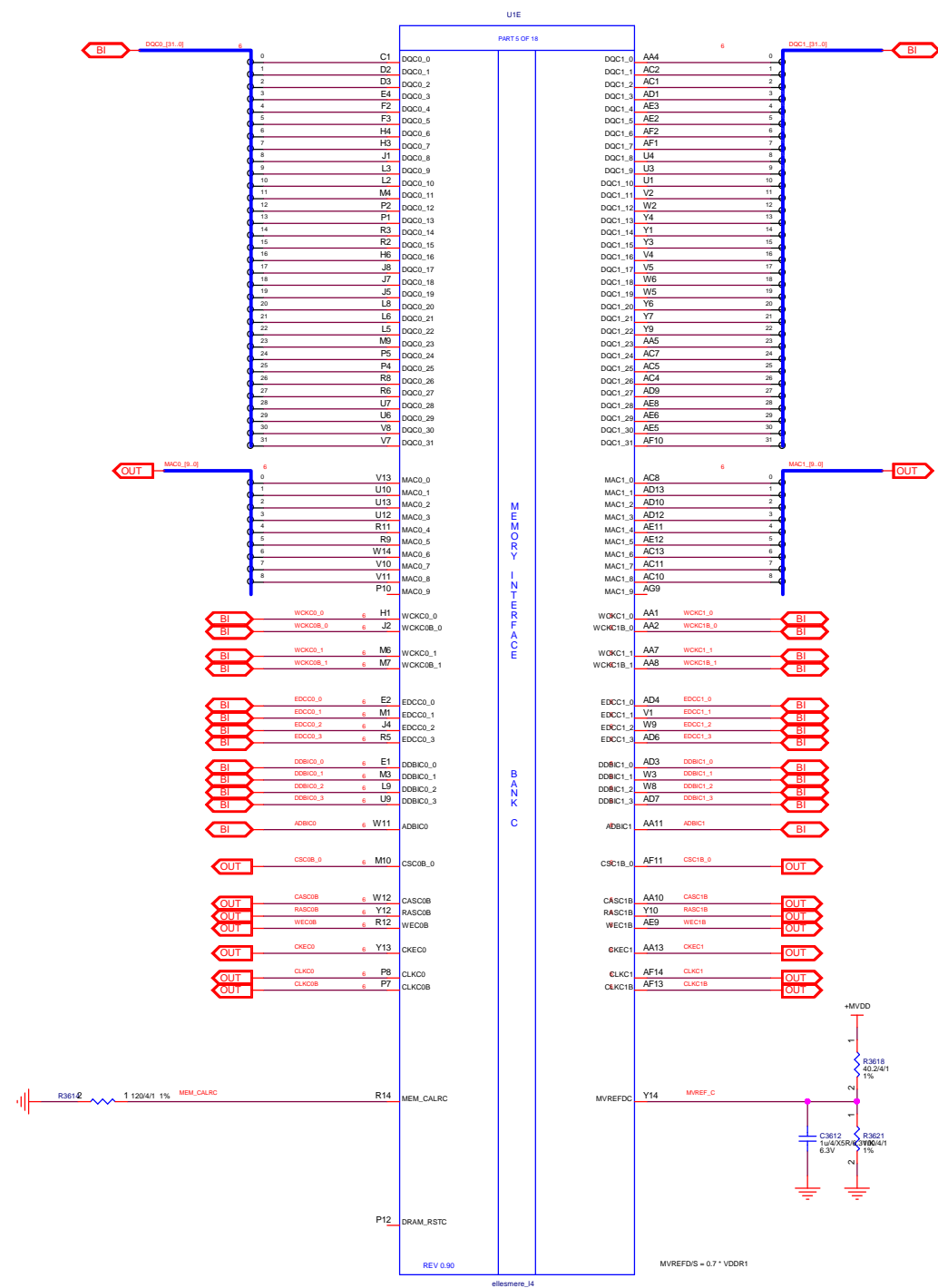
(2) ELLESMERE PCIE INTERFACE



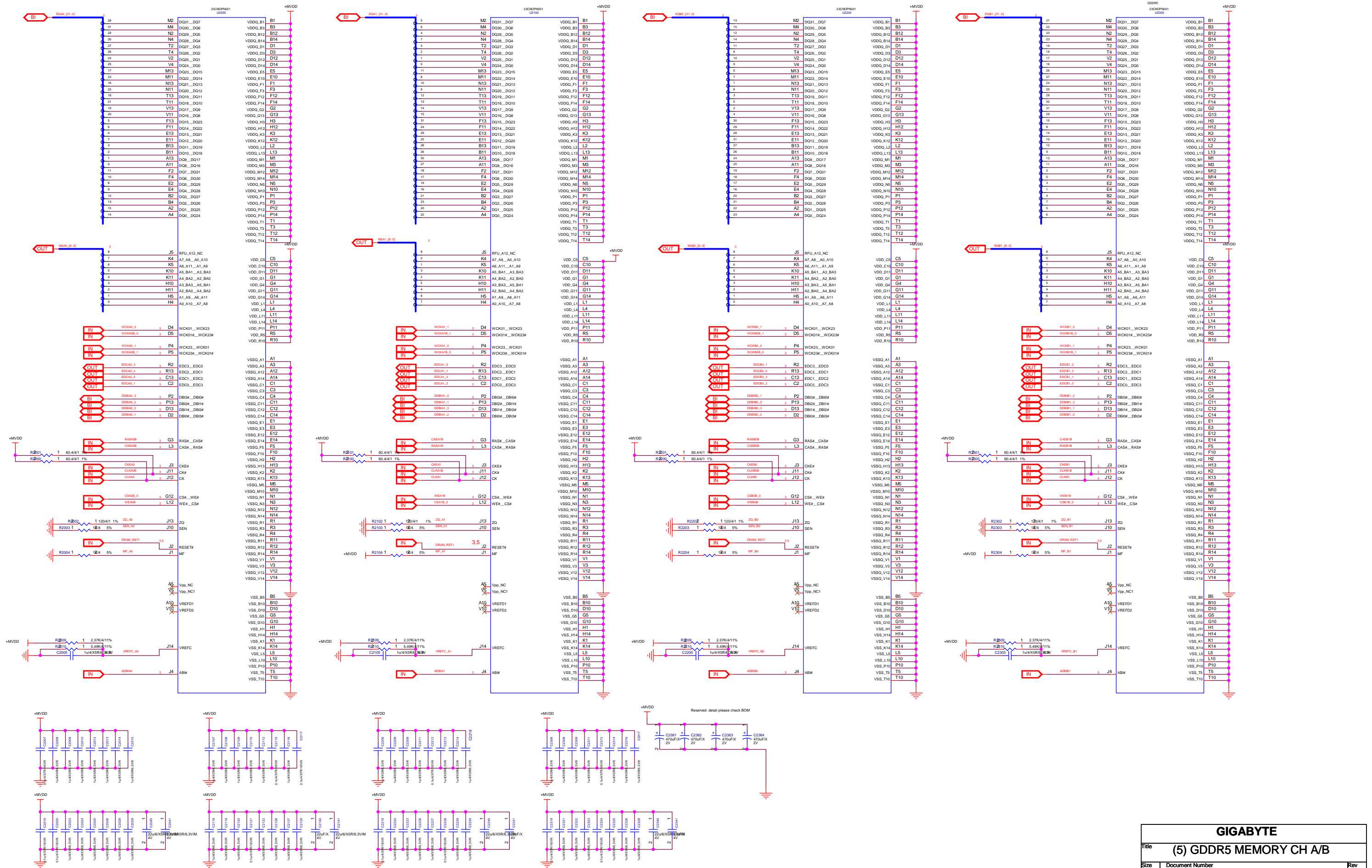
(3) ELLESMERE MEM INTERFACE CH A/B



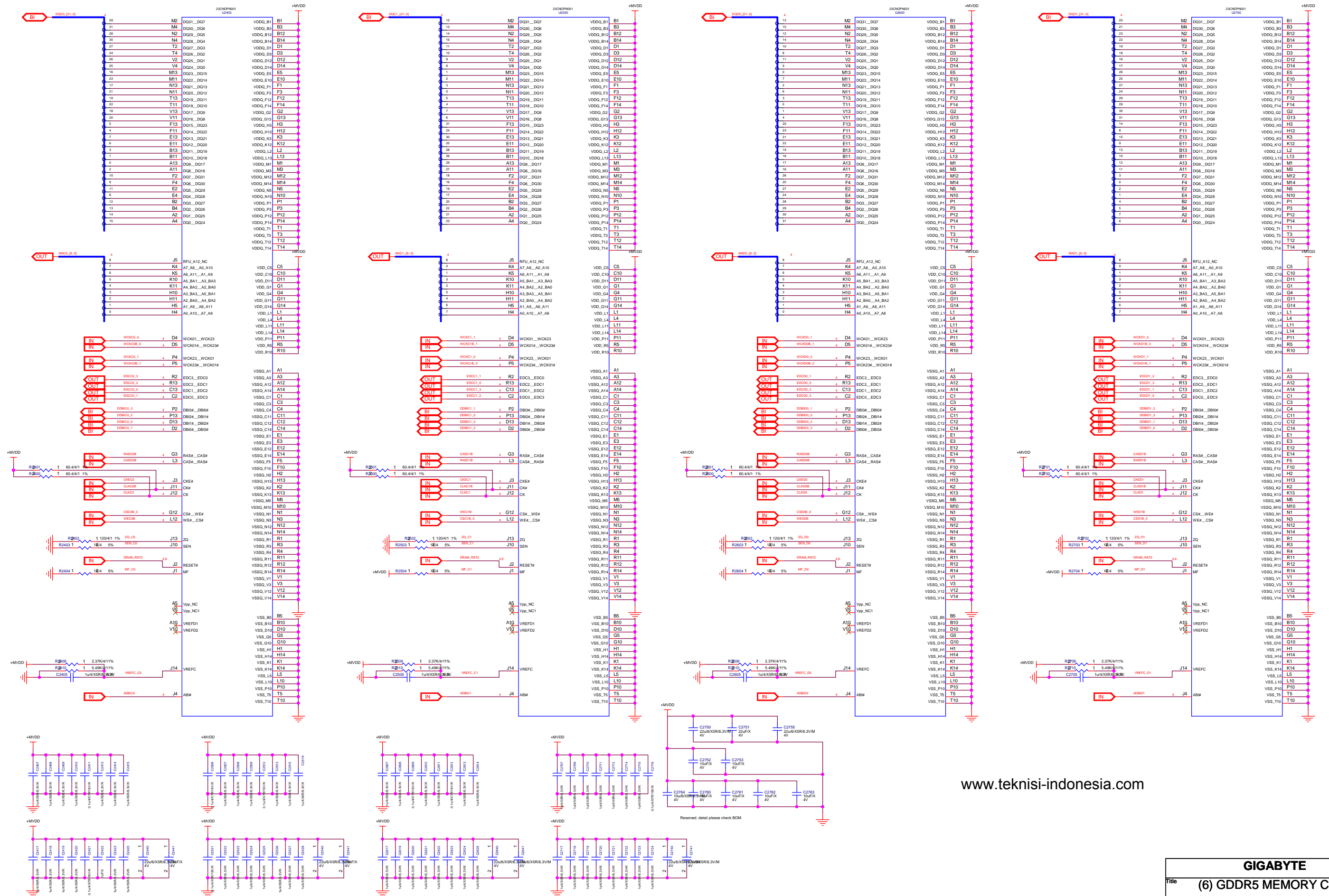
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



(6) GDDR5 MEMORY CH C/D



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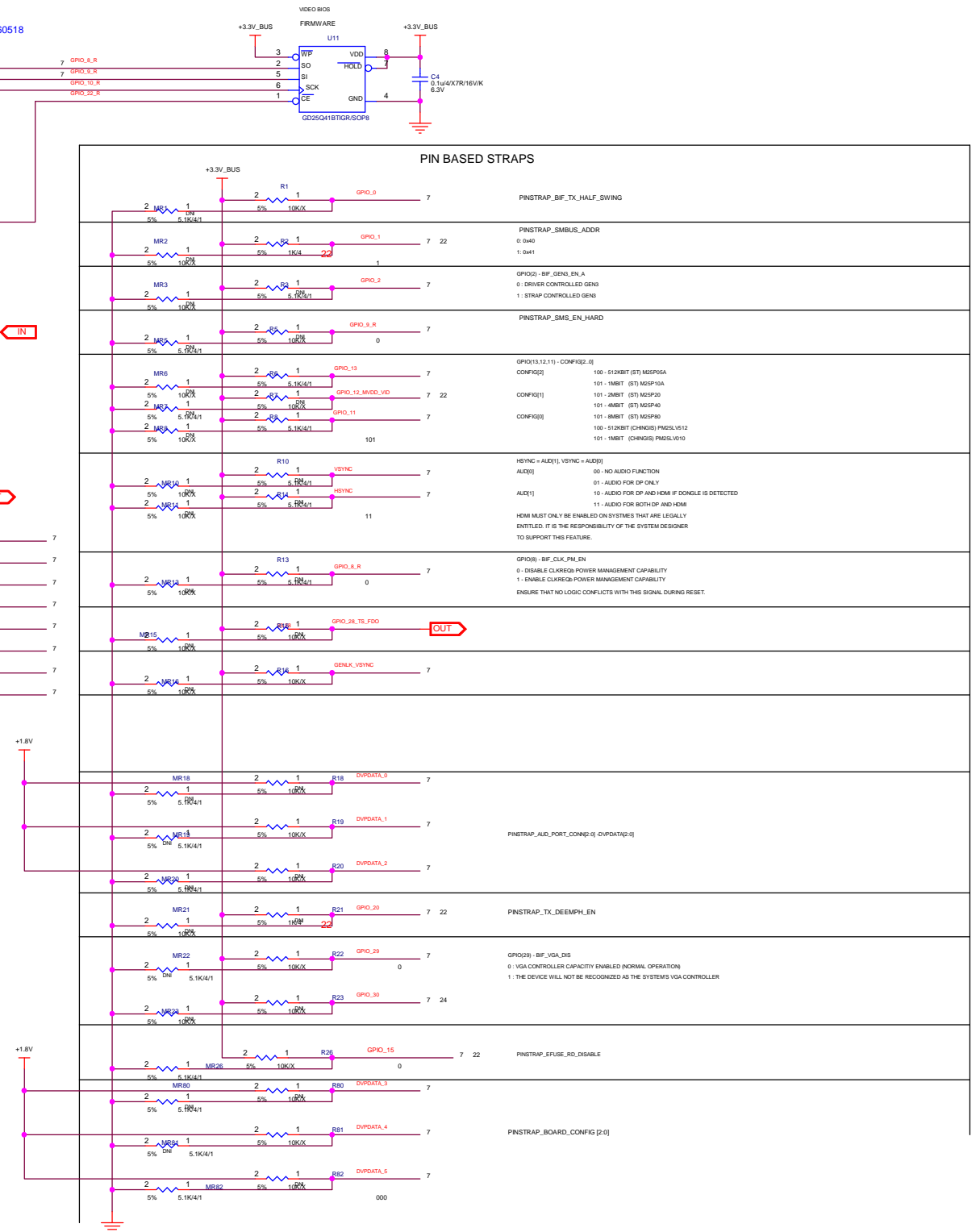
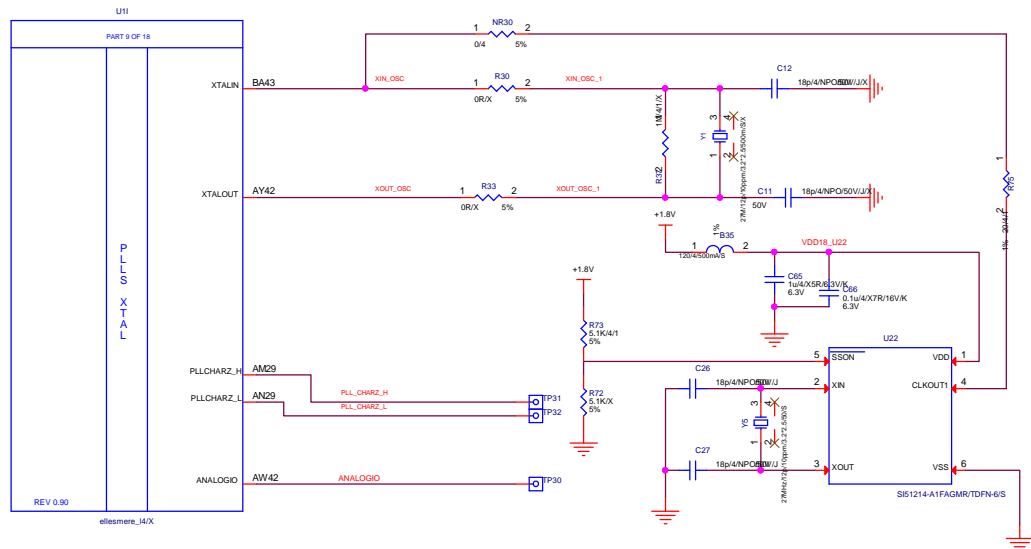
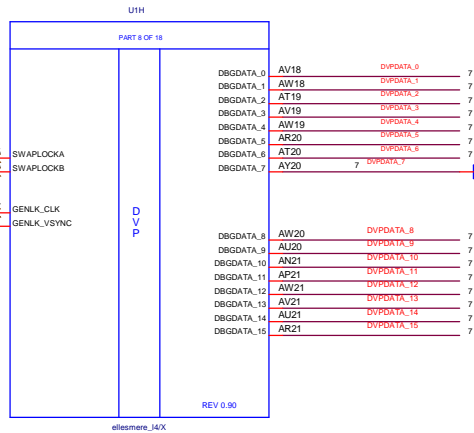
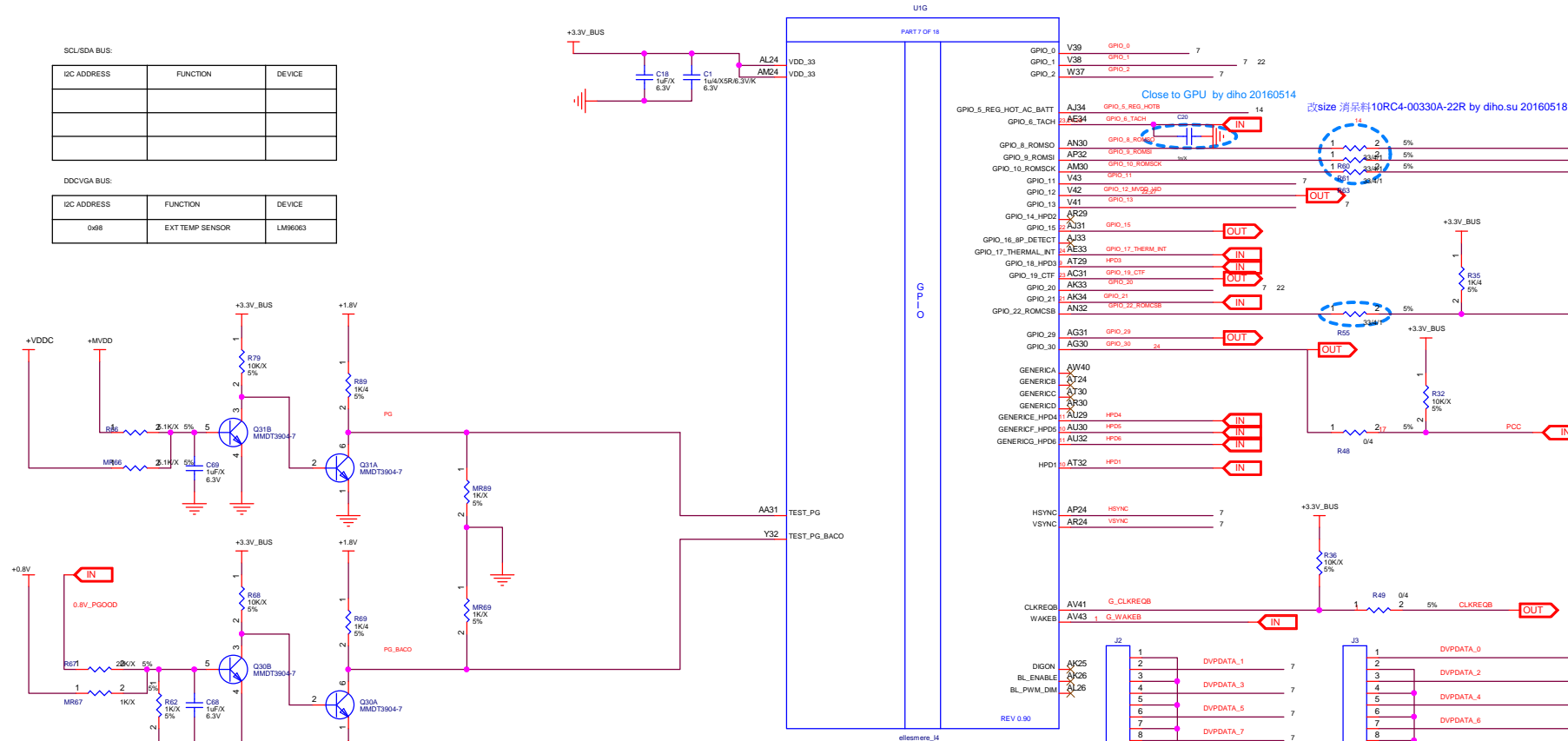
(7) ELLESMERE GPIO STRAP CF XTAL

SC1/SDA BUS:

I2C ADDRESS	FUNCTION	DEVICE

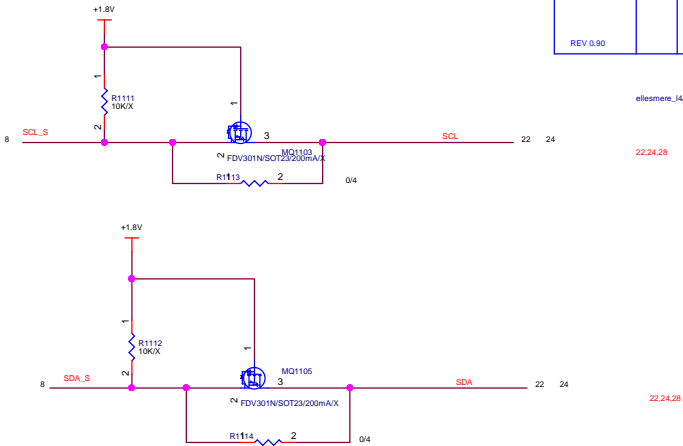
DDCVGA BUS:

I2C ADDRESS	FUNCTION	DEVICE
0x08	EXT TEMP SENSOR	LMM0053



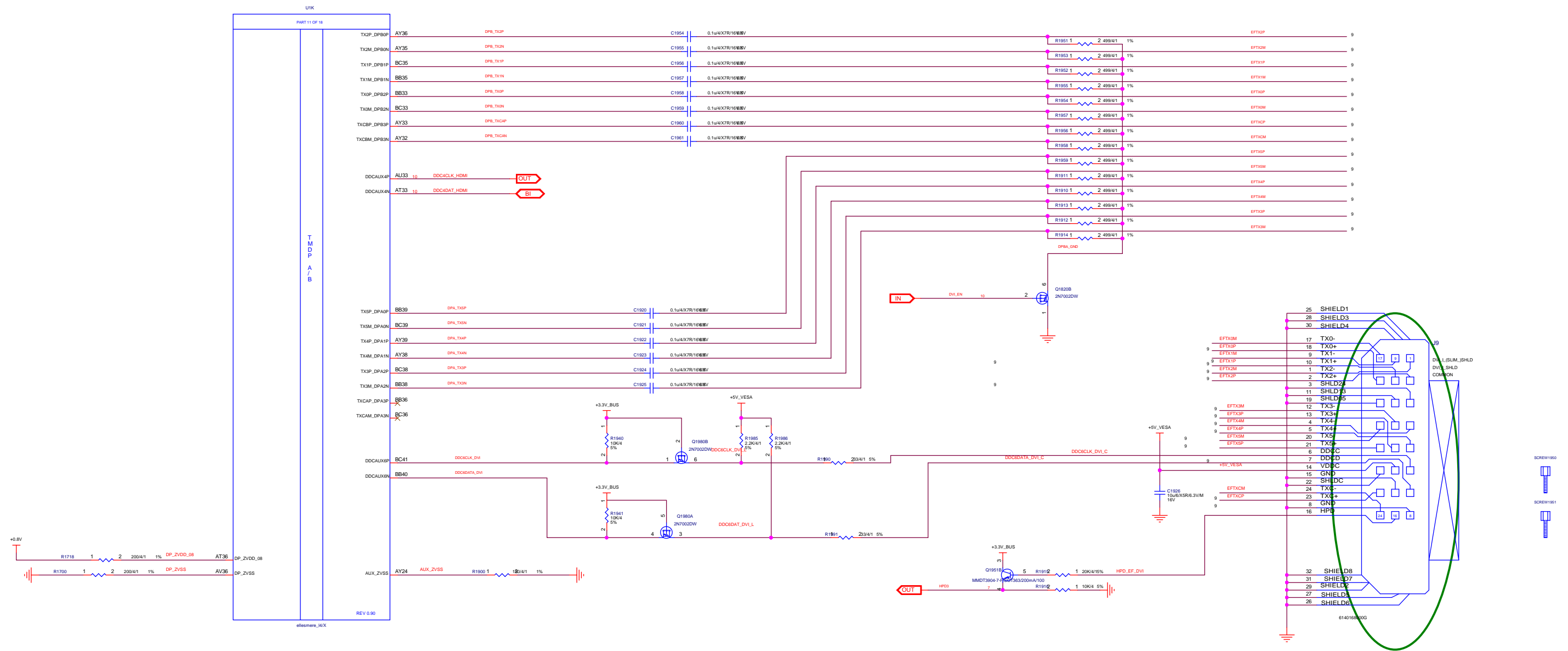
GIGABYTE			
Title ELLESMERE GPIO STRAP CF XTAL			
Size	Document Number		Rev
Custom	GV-RX570GAMING-8GD-MI		1.11
Date:	Friday, June 01, 2018	Sheet	7 of 29

(8) ELLESMERE DAC1 LOCK

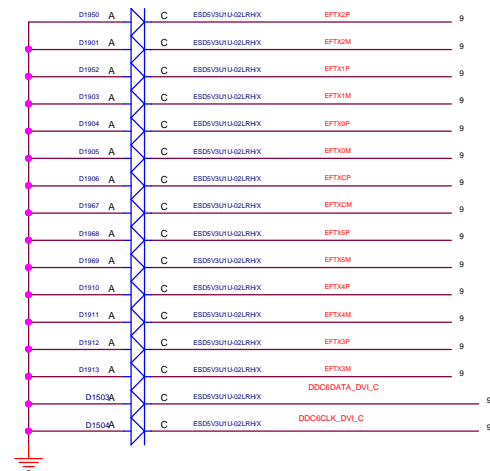


GIGABYTE			
Title ELLESMEERE DAC1 LOCK			
Size	Document Number		Rev
Custom	GV-RX570GAMING-8GD-MI		1.11
Date:	Friday, June 01, 2018	Sheet	8 of 29

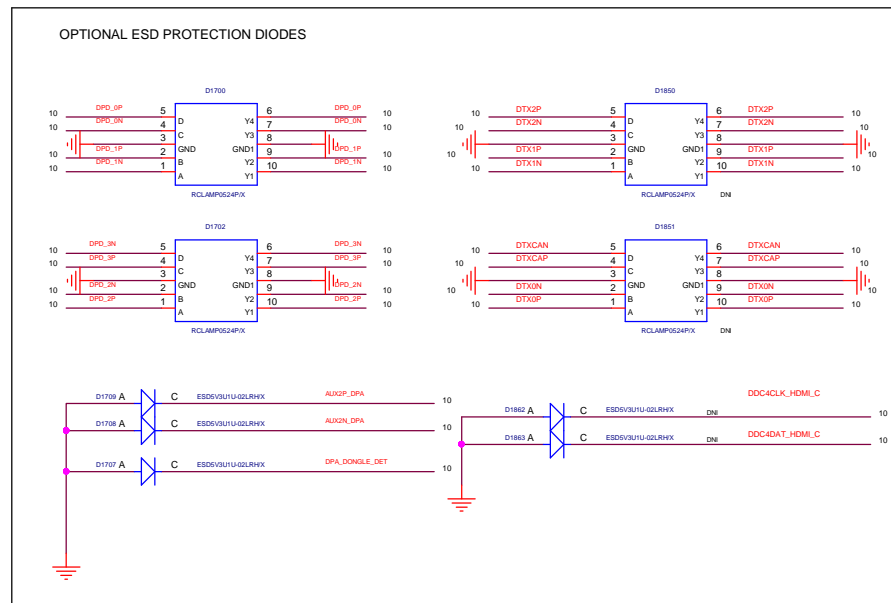
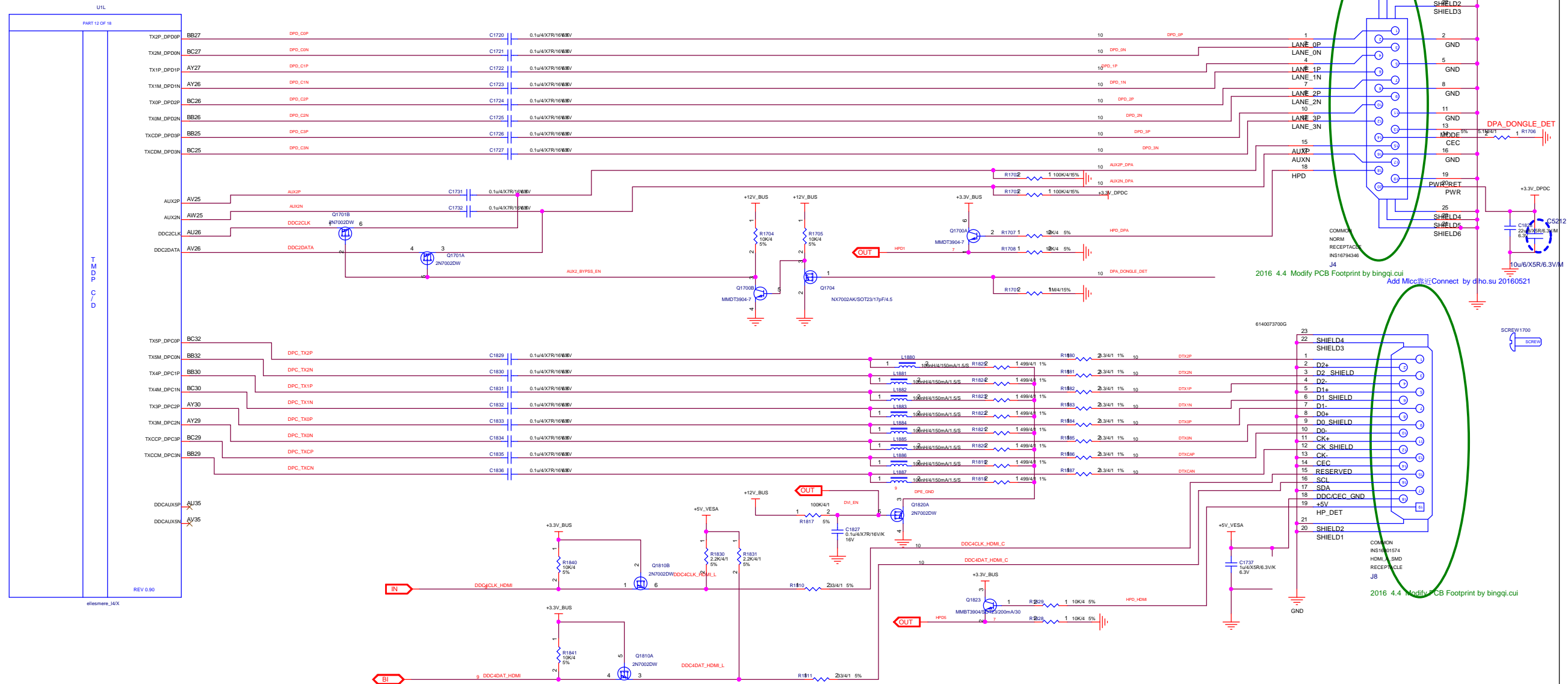
(9) ELLESMERE TMDP A/B

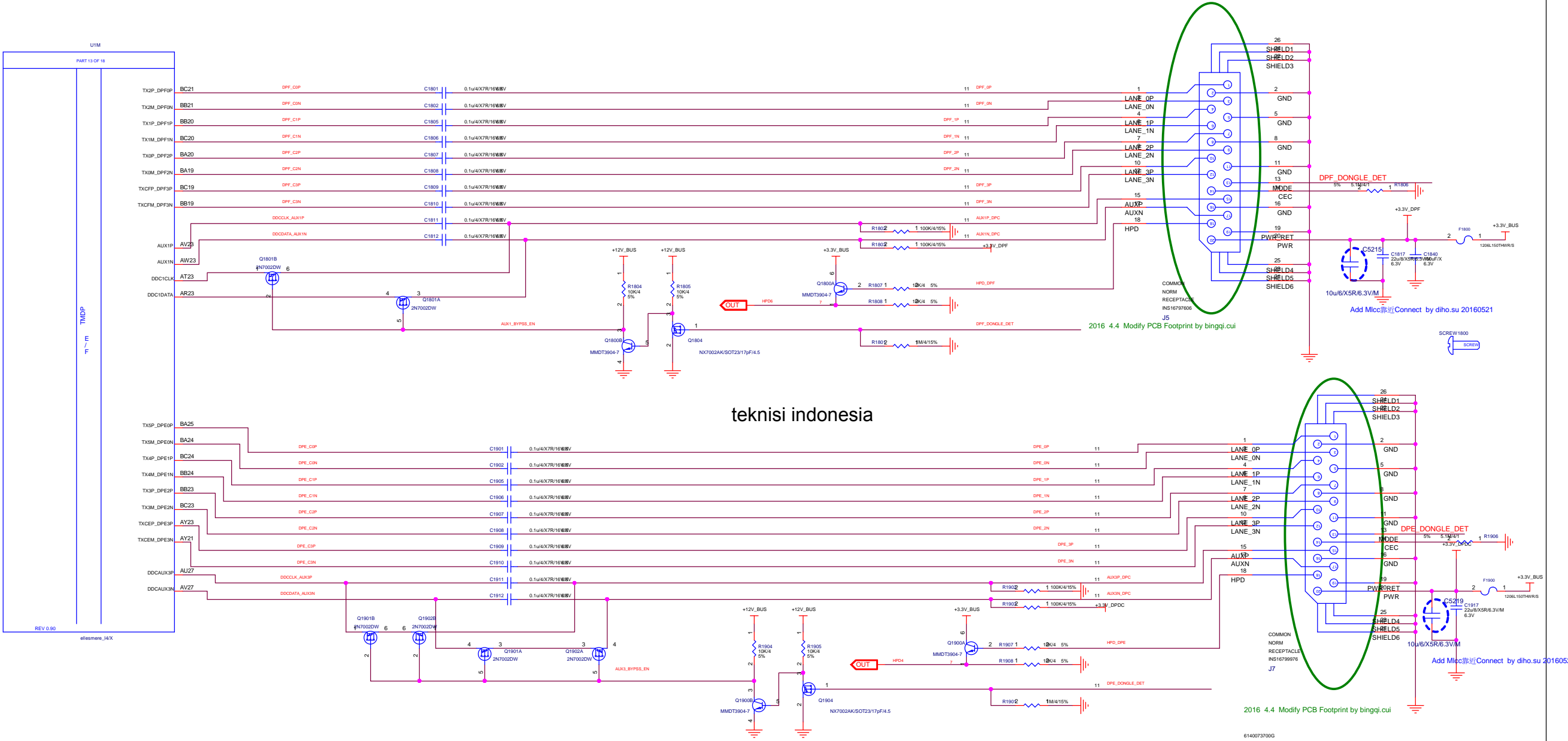


OPTIONAL ESD PROTECTION DIODES



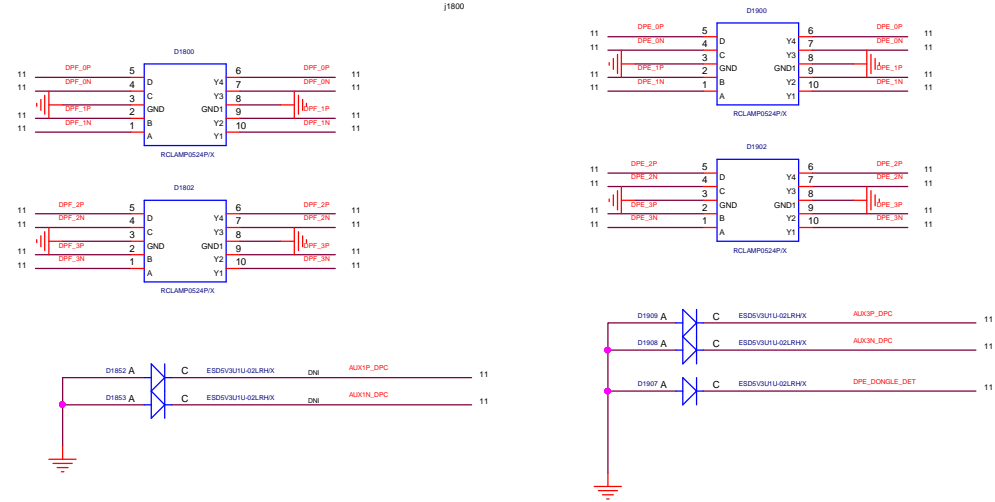
(10) ELLESMERE TMDP C/D





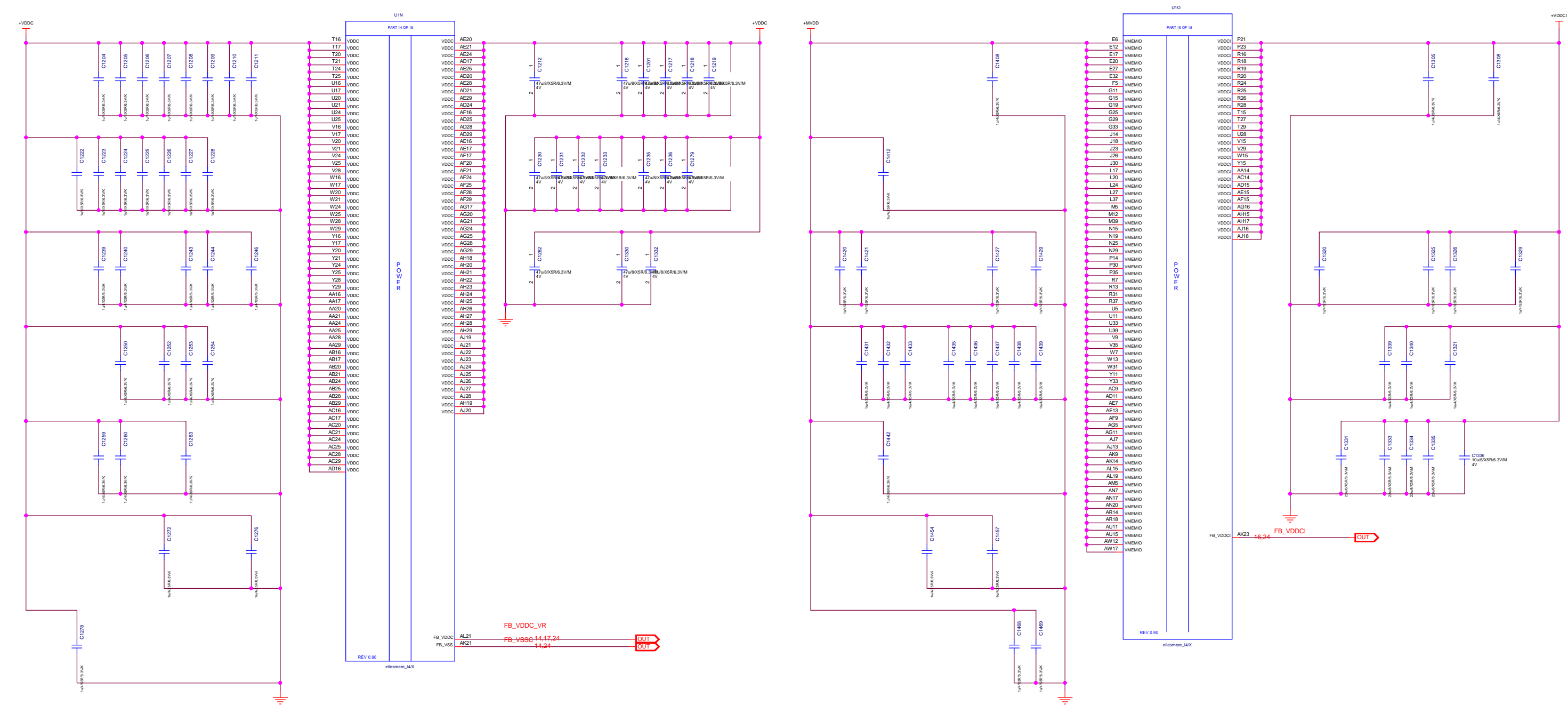
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OPTIONAL ESD PROTECTION DIODES

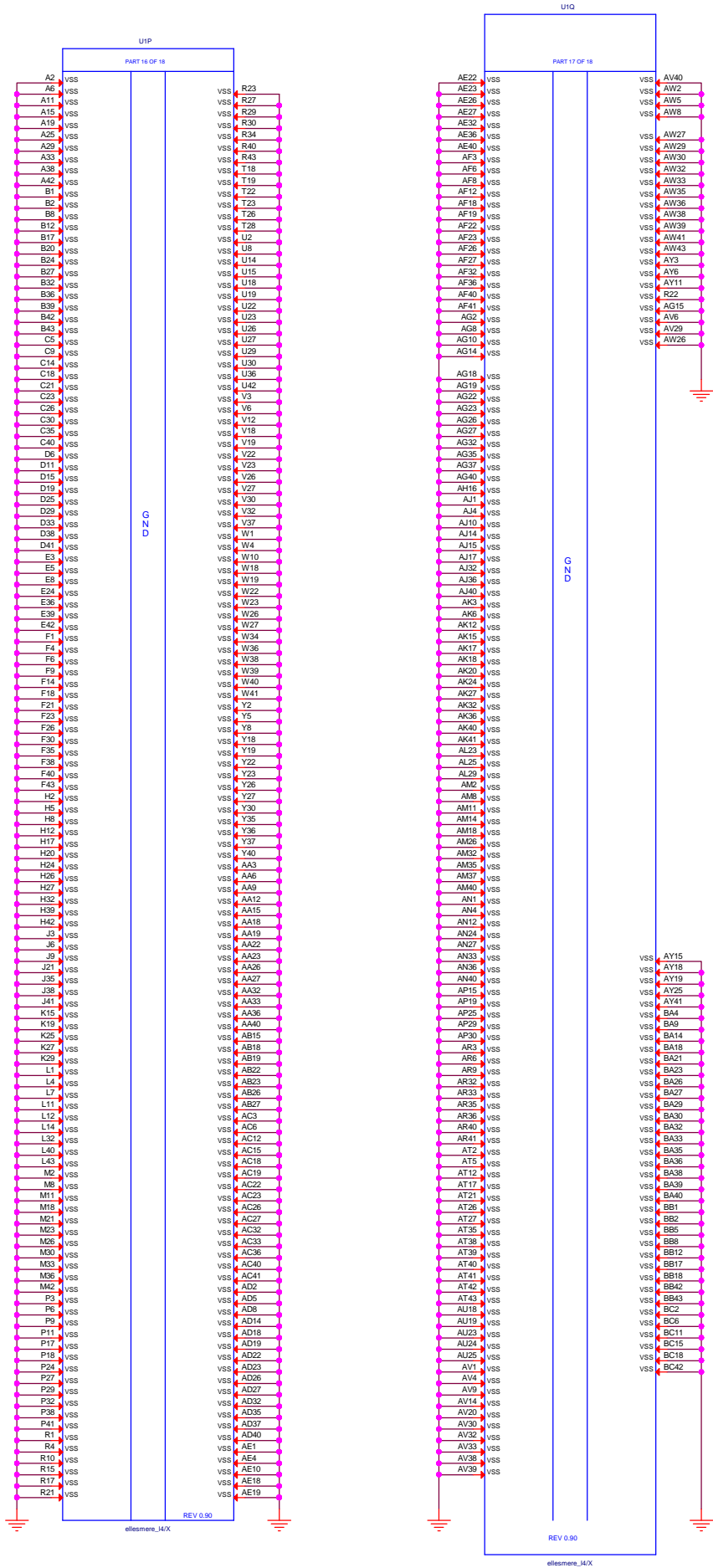


GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size Custom	Document Number GV-RX570GAMING-8GD-MI	Rev 1.11	
Date: Friday, June 01, 2018	Sheet 11	of 29	

(12) ELLESMERE POWER

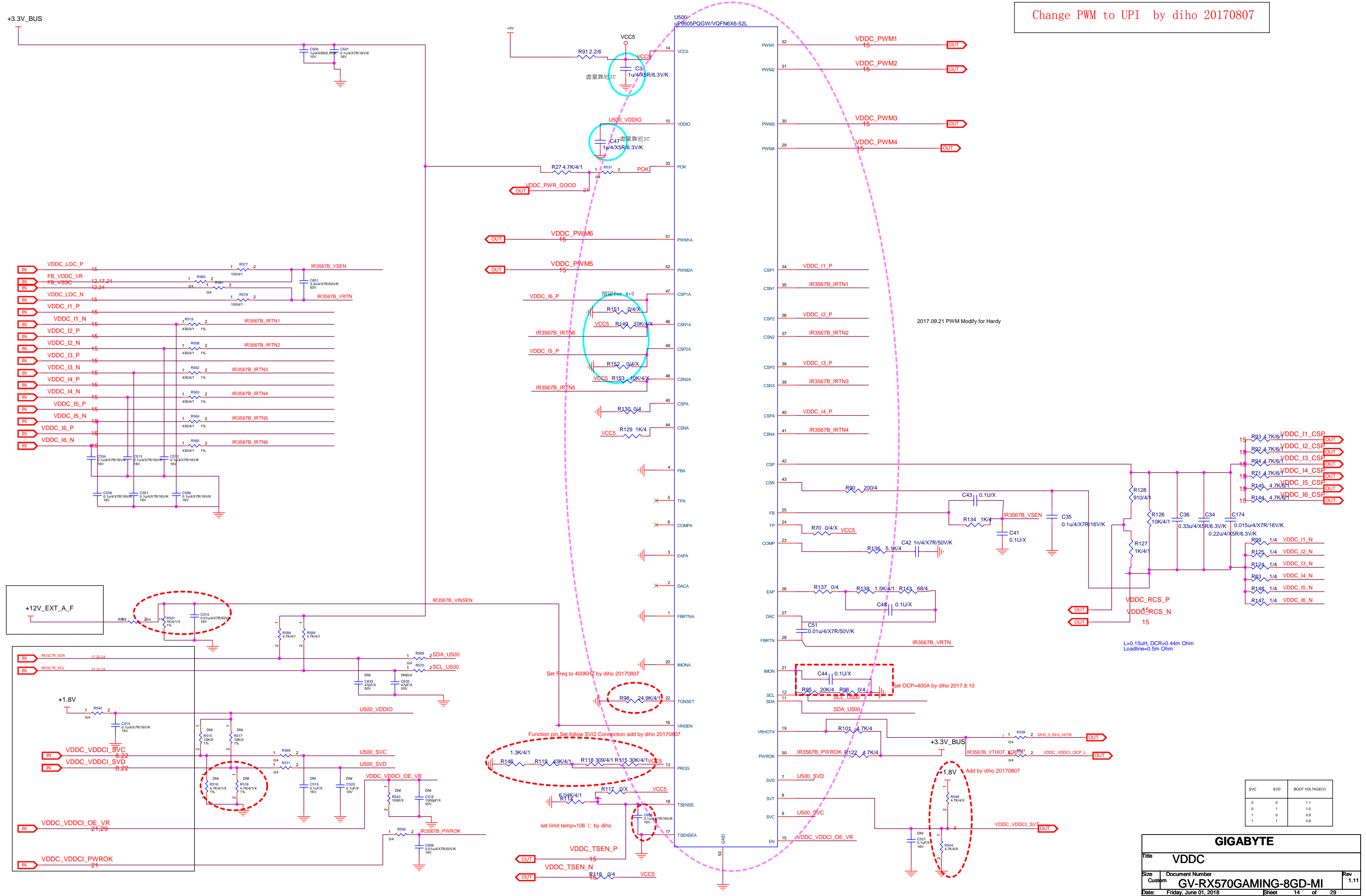


(13) ELLESMERE GROUND



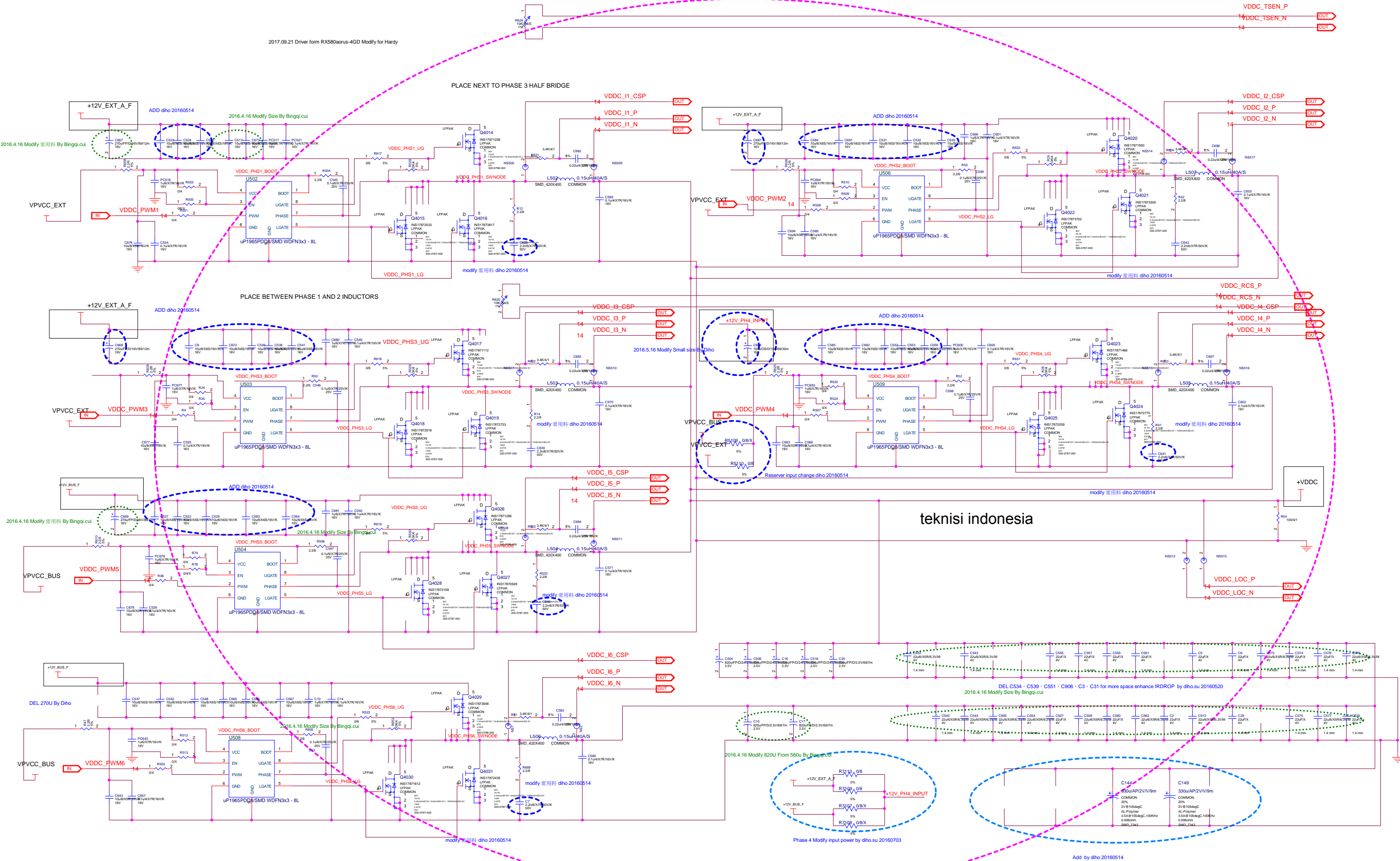
GIGABYTE			
Title ELLESMERE GND			
Size Custom	Document Number GV-RX570GAMING-8GD-MI		Rev 1.11
Date: Friday, June 01, 2018	Sheet 13	of 29	

Change PWM to UPI by diho 20170807

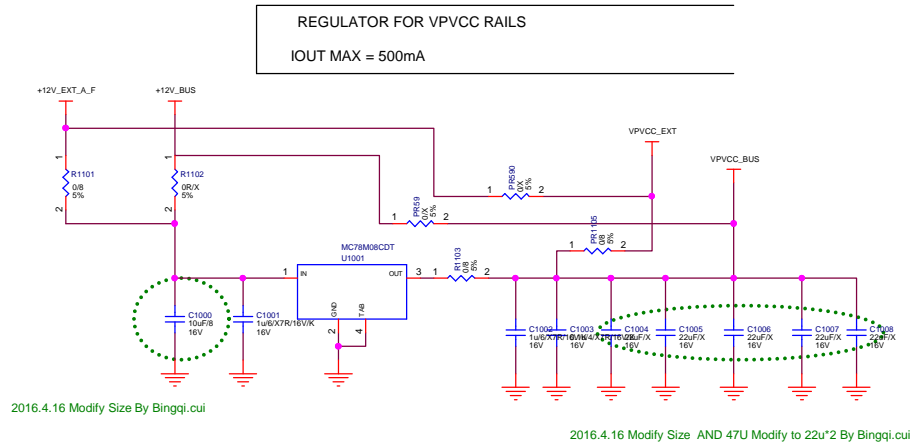
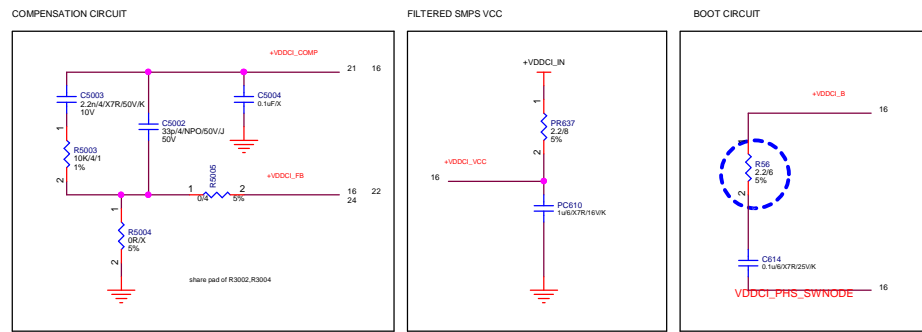
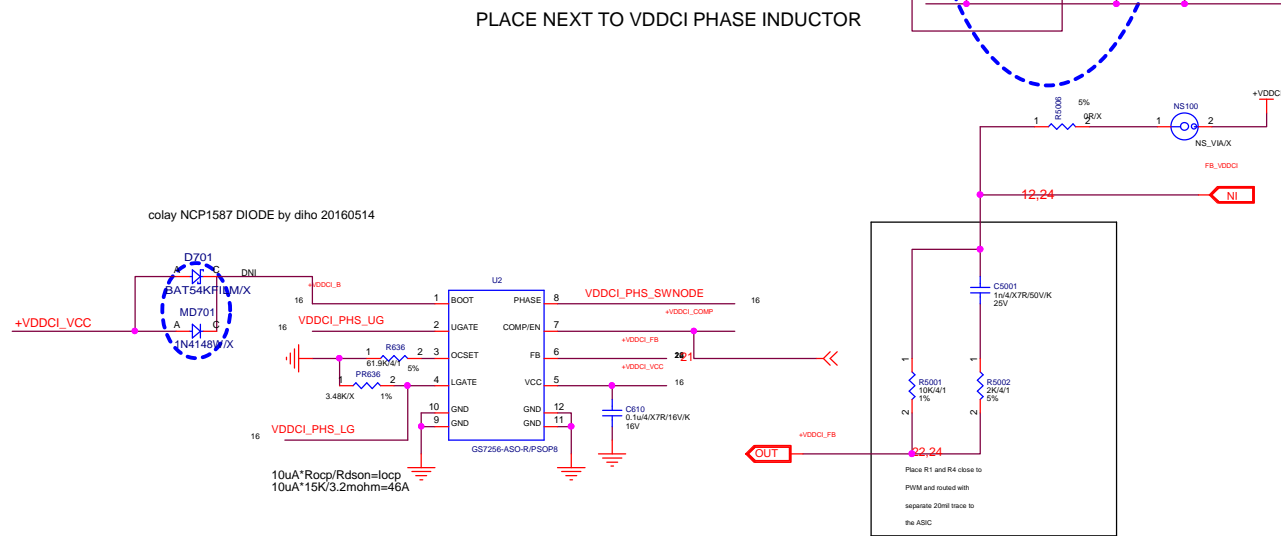
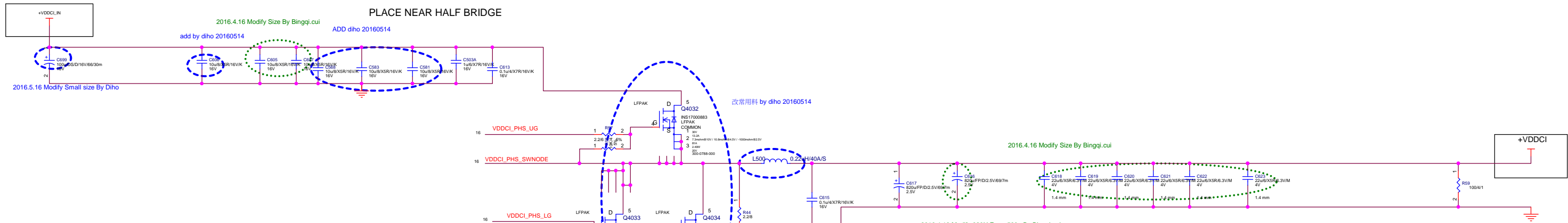


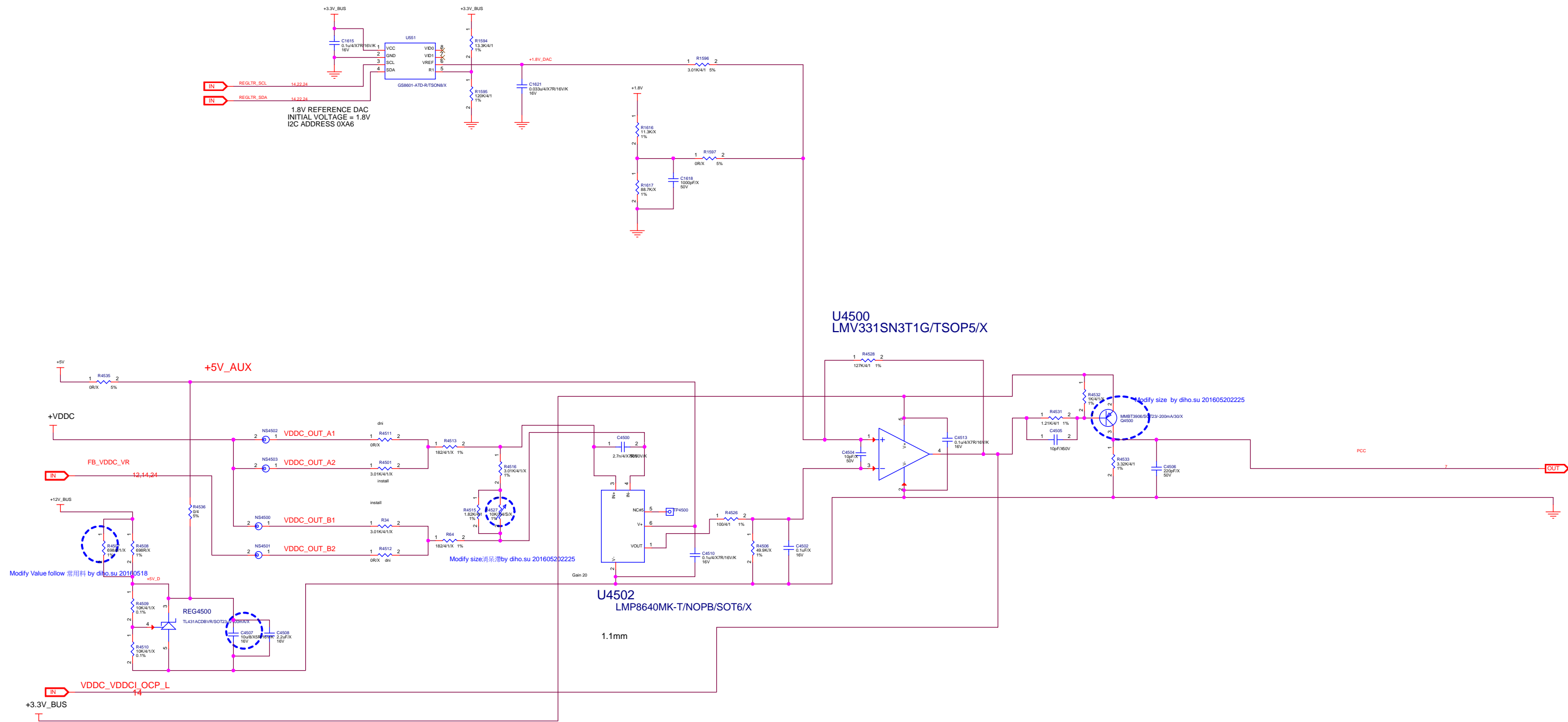
SVC	SVD	BOOT VOLTAGE(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

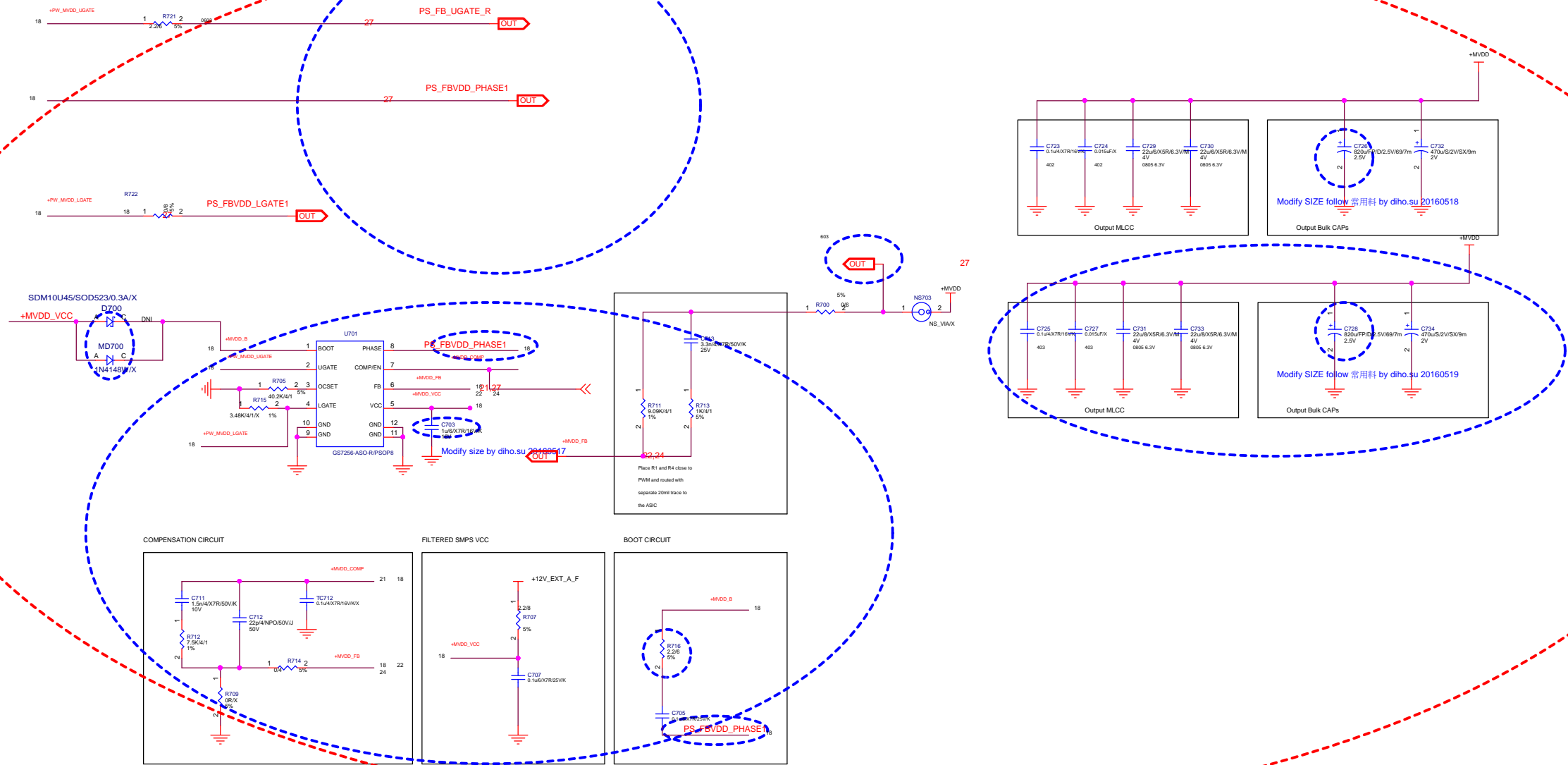
2017.09.21 Driver form RX580acrus-4GD Modify for Hardy



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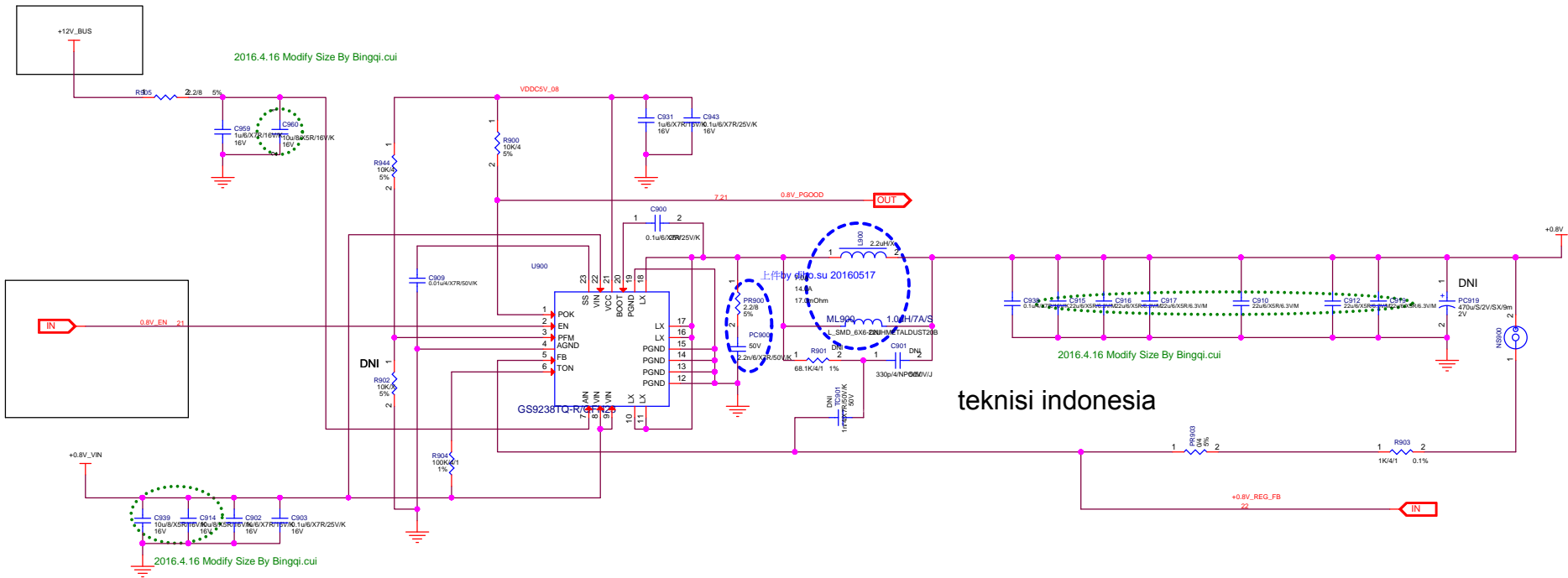




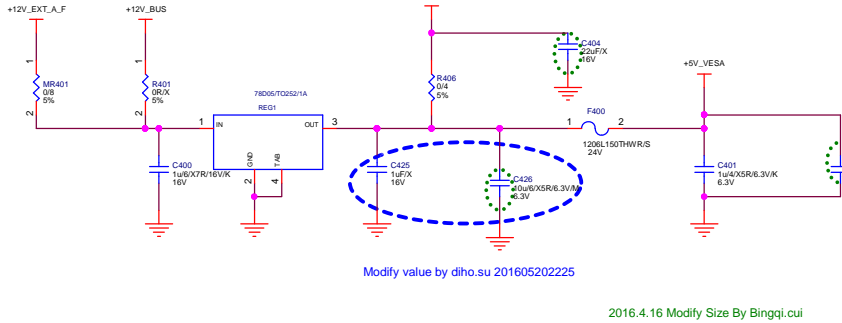
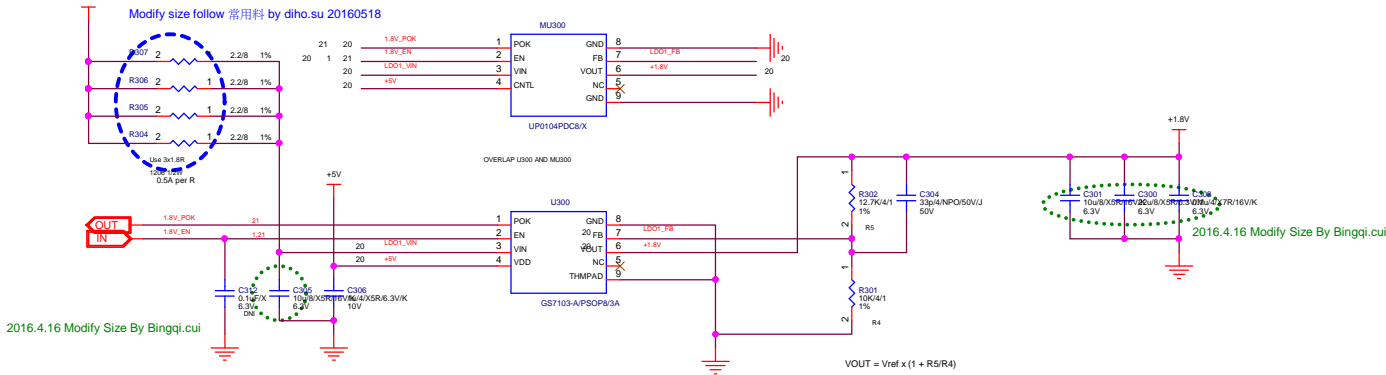
/X Single phase power by diho.su 20160517

上件 by xiang.l@20160526

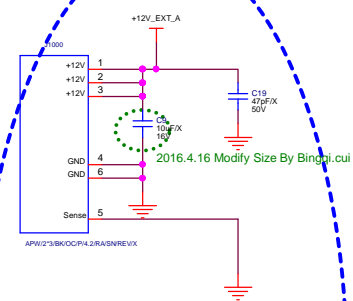
GIGABYTE			
Title		MVDD	
Size	Document Number	GV-RX570GAMING-8GD-MI	Rev 1.11
Custom			
Date:	Friday, June 01, 2018	Sheet	18 of 29



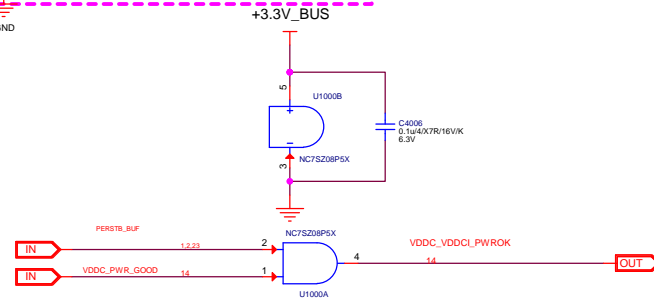
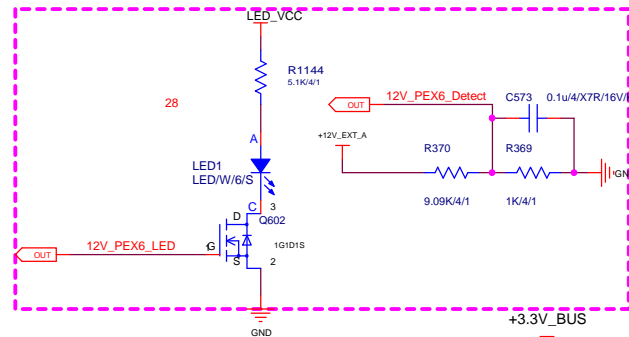
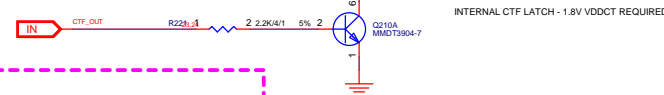
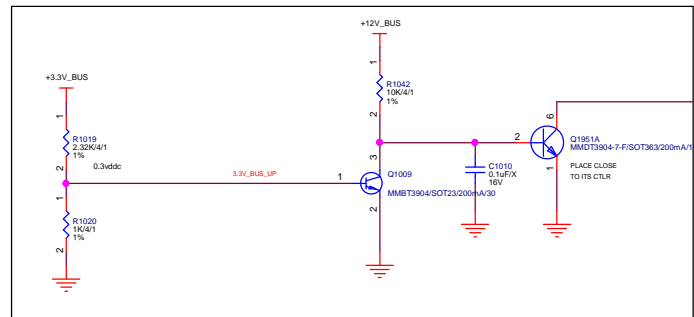
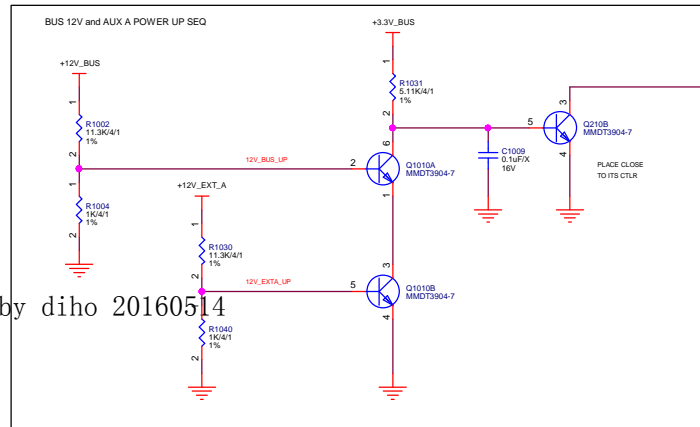
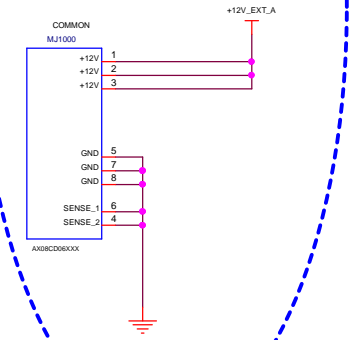
(18) SMALL RAIL REGULATORS



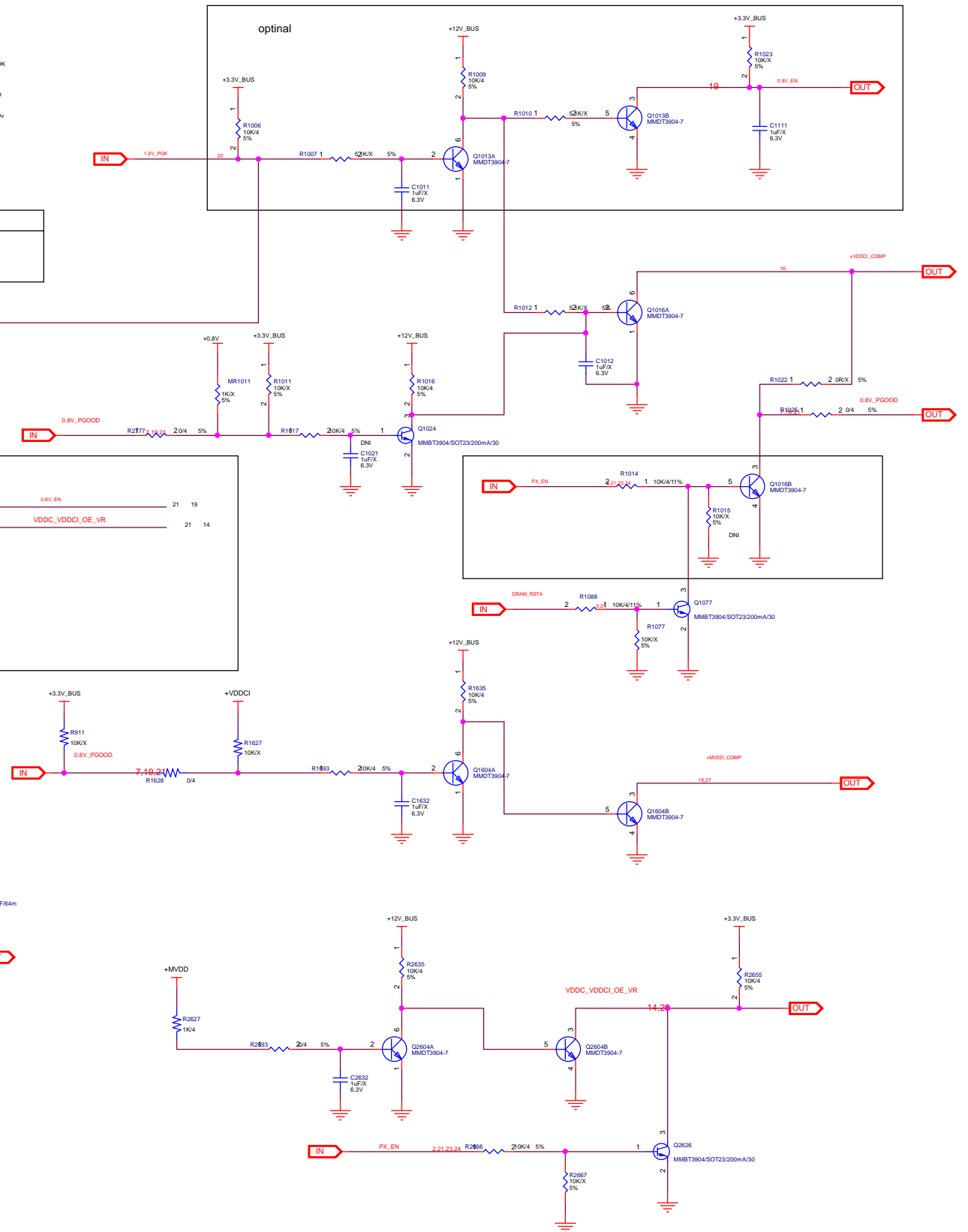
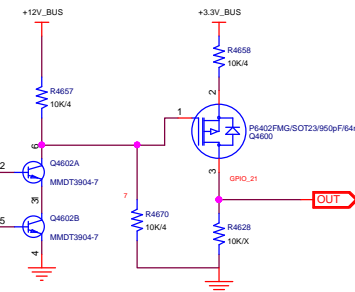
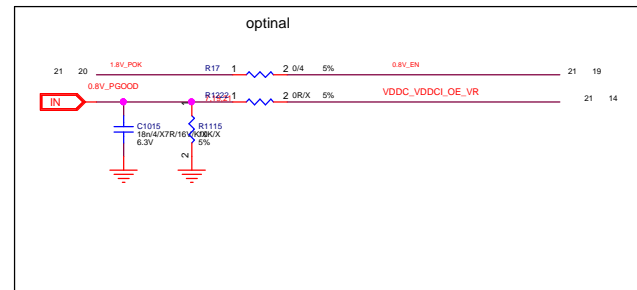
(19) POWER MANAGEMENT

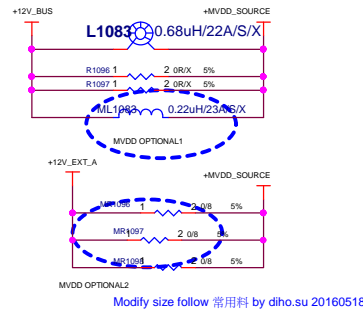
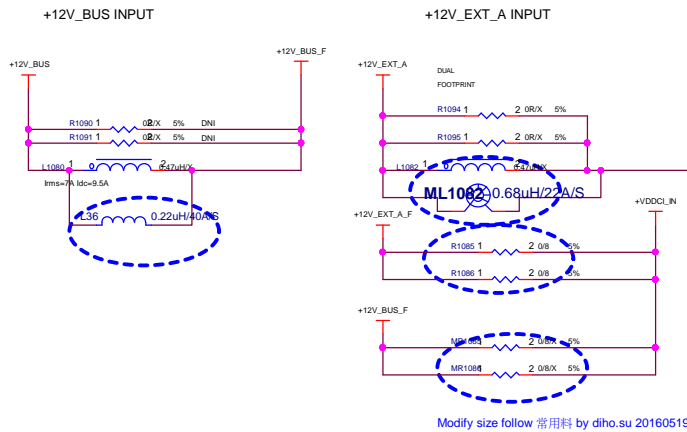
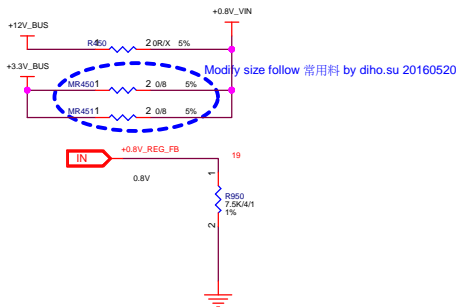
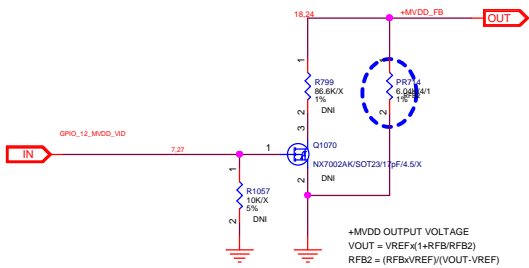
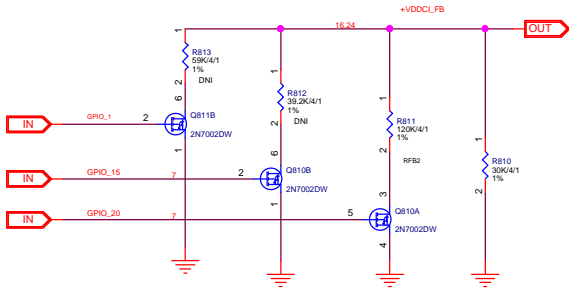
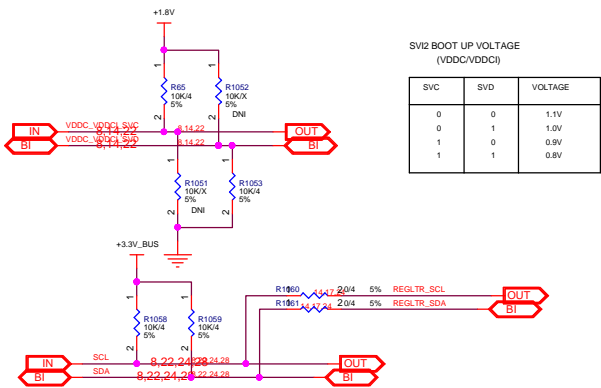


6PIN&8PIN CONNECT COLAY by diho 20160514



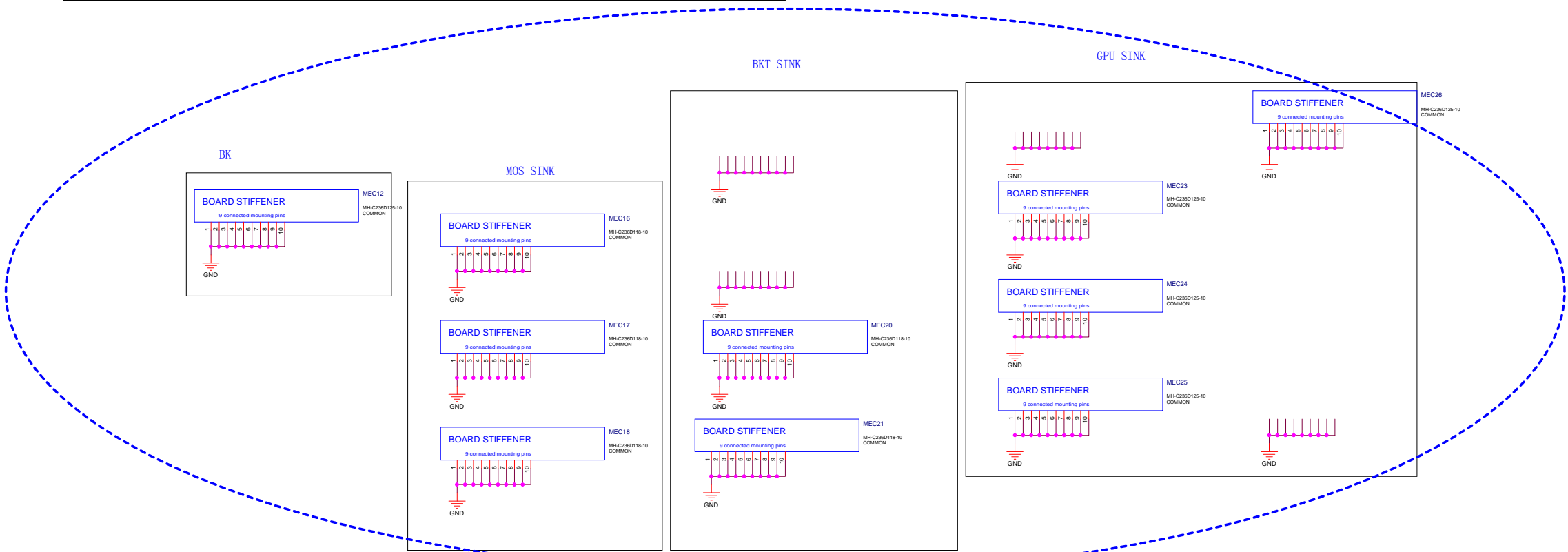
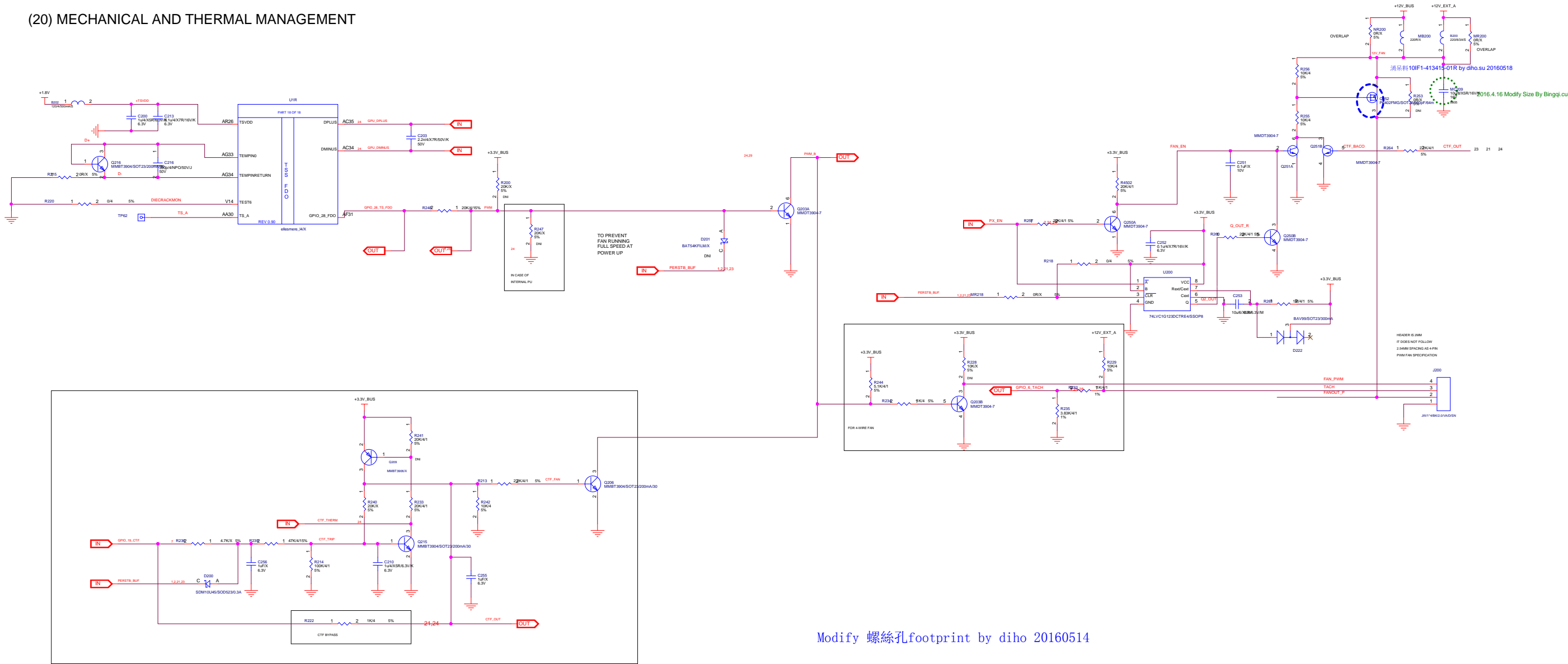
POWER UP SEQUENCE			
BUS RAILS (3.3W12V UP) -> +1.8V -> 0.935V		BIF_VDDC VDDC -> VDDCI M/VD	



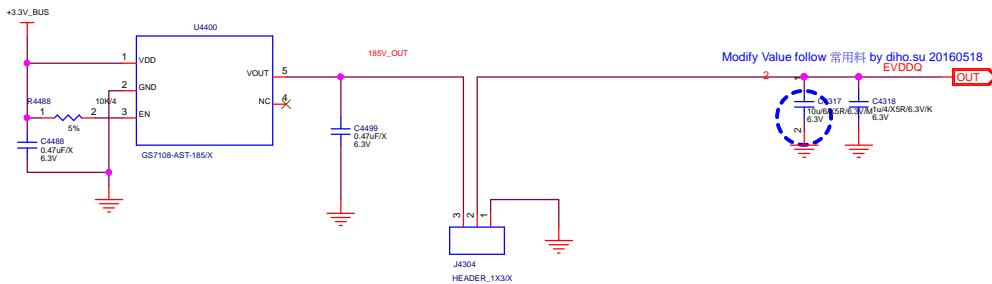
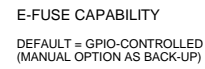
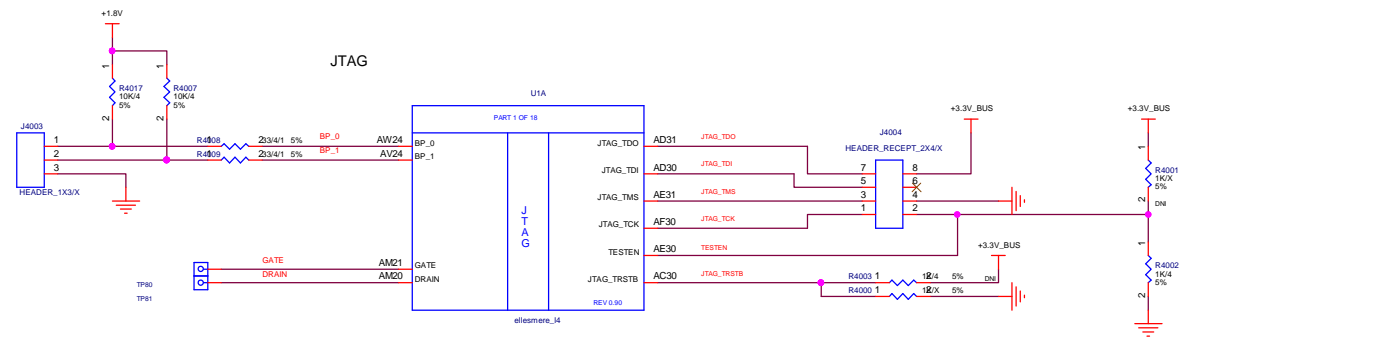


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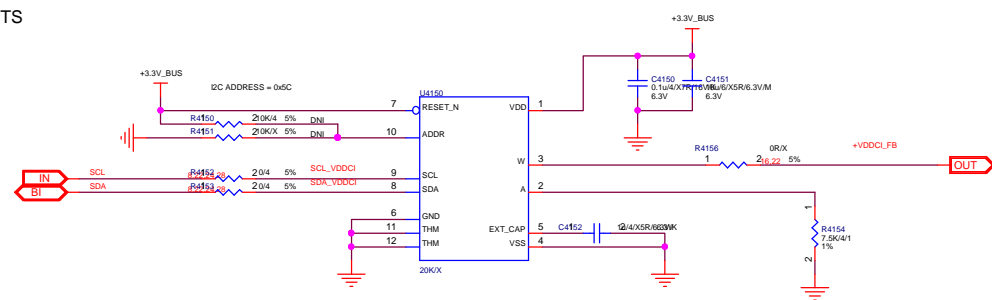
(20) MECHANICAL AND THERMAL MANAGEMENT



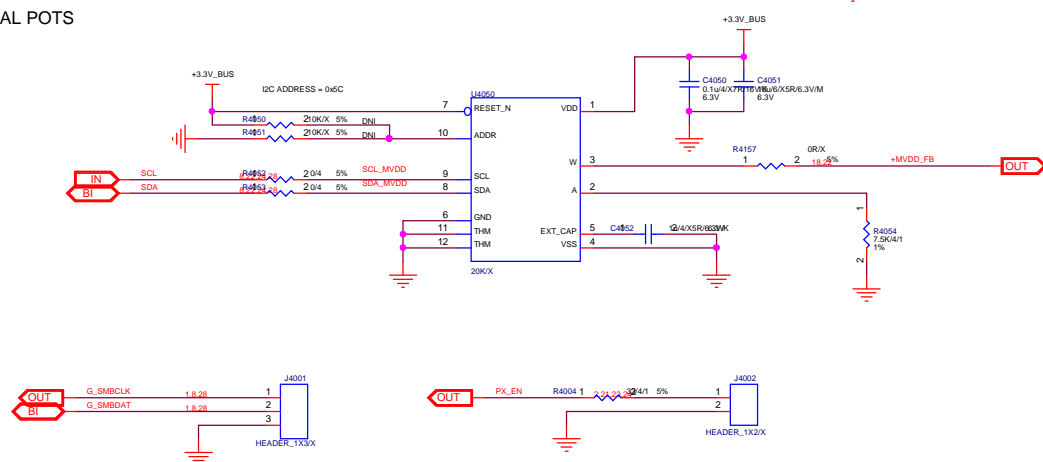
(21) DEBUG CIRCUITS



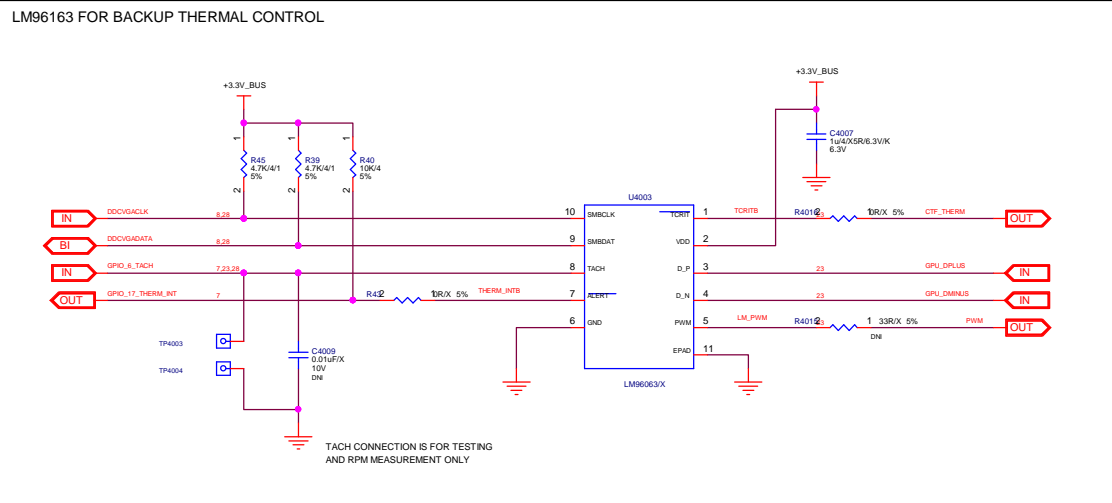
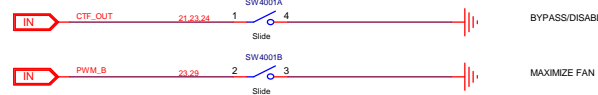
DIGITAL POTS



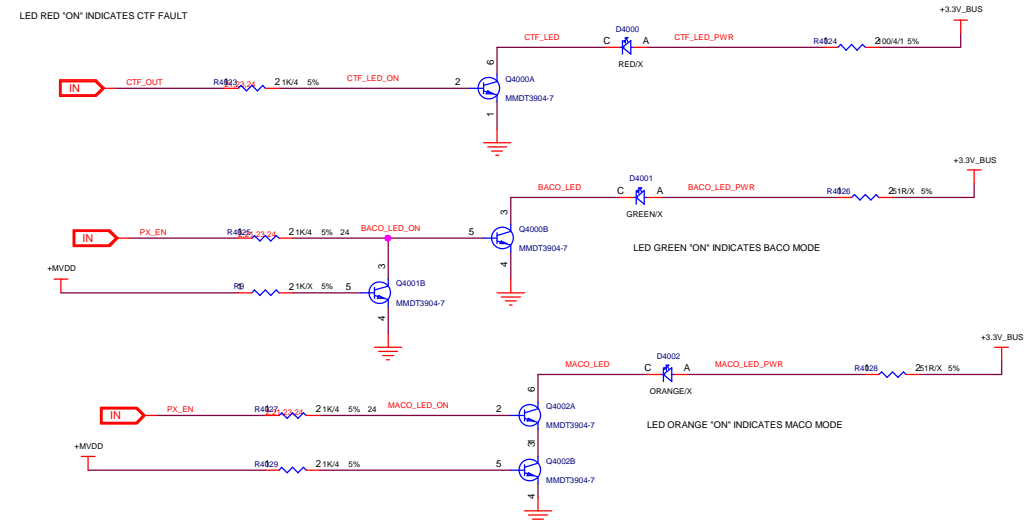
DIGITAL POTS



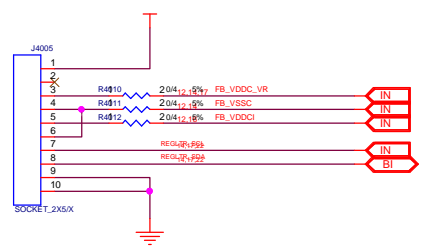
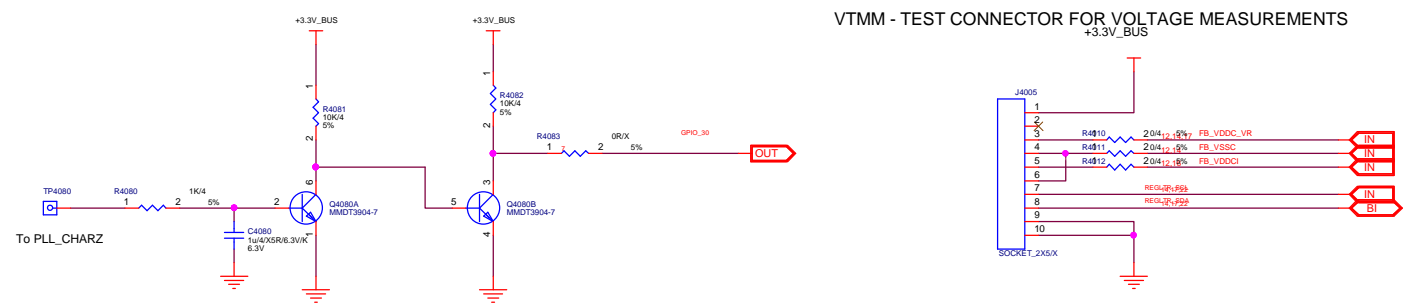
SWITCHES



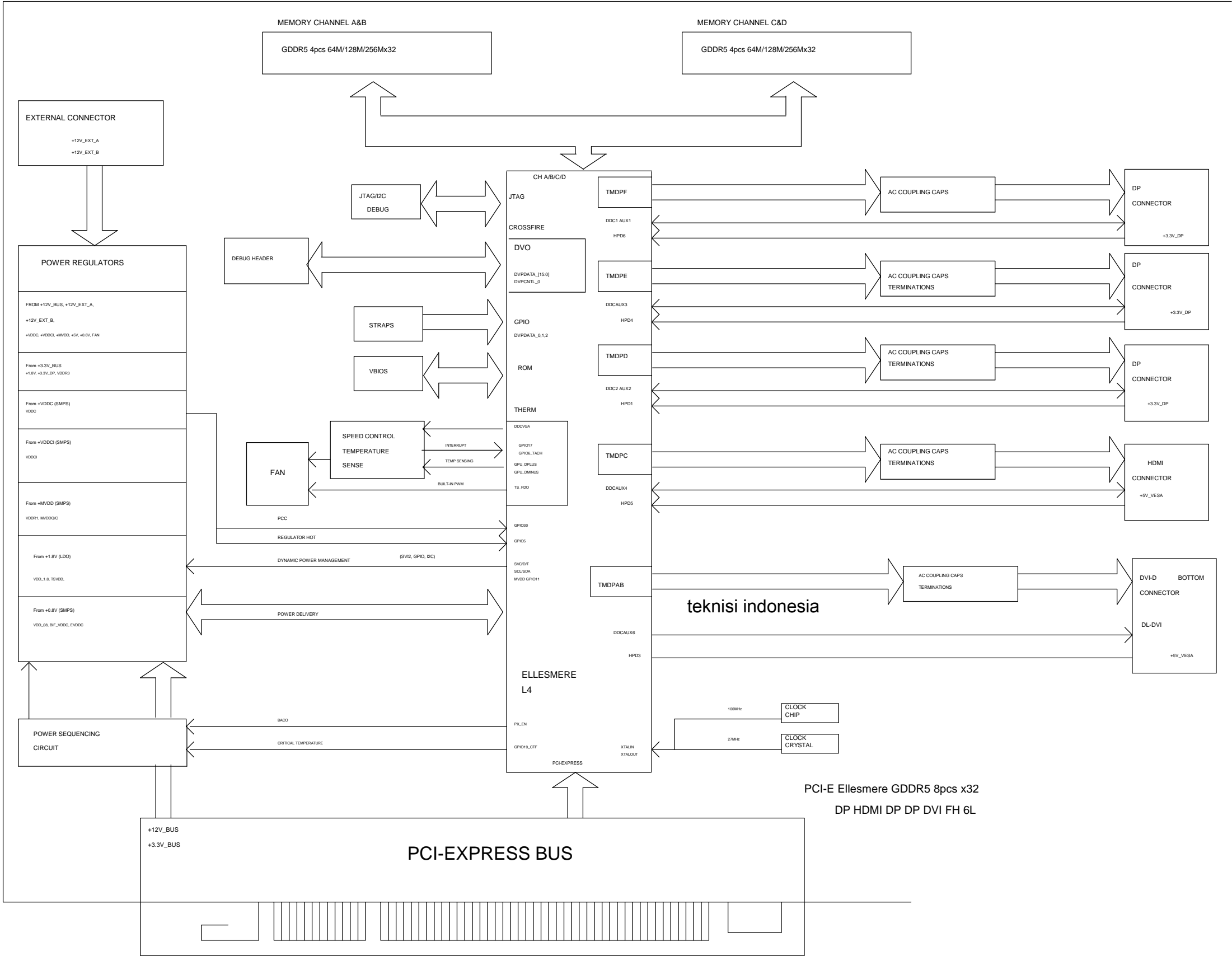
LED LIGHTS



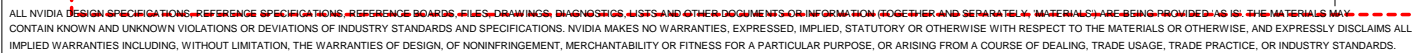
VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS



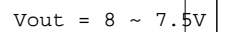
GIGABYTE			
Title DEBUG CIRCUITS			
Size Custom	Document Number GV-RX570GAMING-8GD-MI		Rev 1.11
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0	00A	08/28/2018	
1	00B	10/18/2018	1. Add GPIO1 for dynamic VDDCI 2. update VDDCI driver sequence
2	00C	03/22/2019	HDMI: - add series resistor R1880 ~R1887 - add pull down inductor L1880 ~L1887 remove C405, C403, VR400,C410,C414,R405
3	00D	04/18/2019	Modify GBT Design

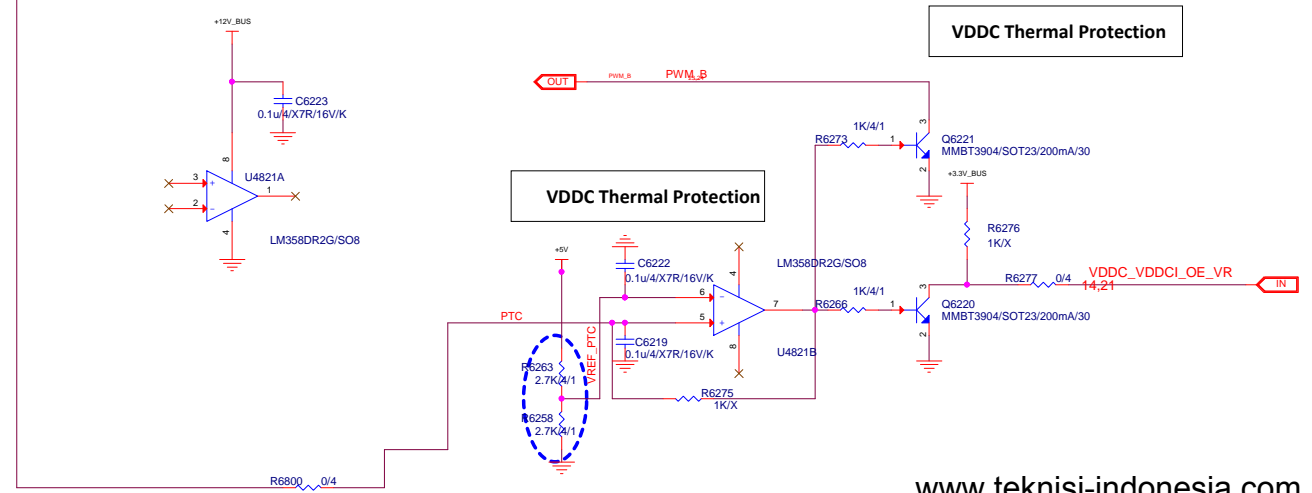
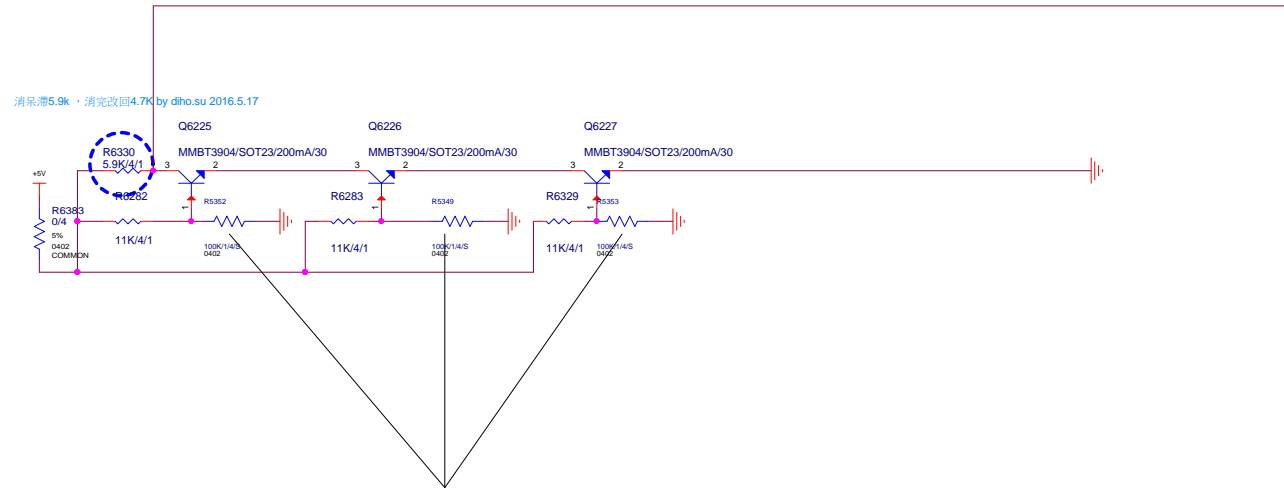


GIGABYTE			
Title FBVDD			
Size	Document Number		Rev
	GV-RX570GAMING-8GD-MI		1.11
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Title										
Size Document Number Rev										
CustomLED	GV-RX570GAMING-8GD-MI _H									1.11
Date:	Friday, June 01, 2018				Sheet	28	of	29		

Add MOS VRHOT CIRCUIT

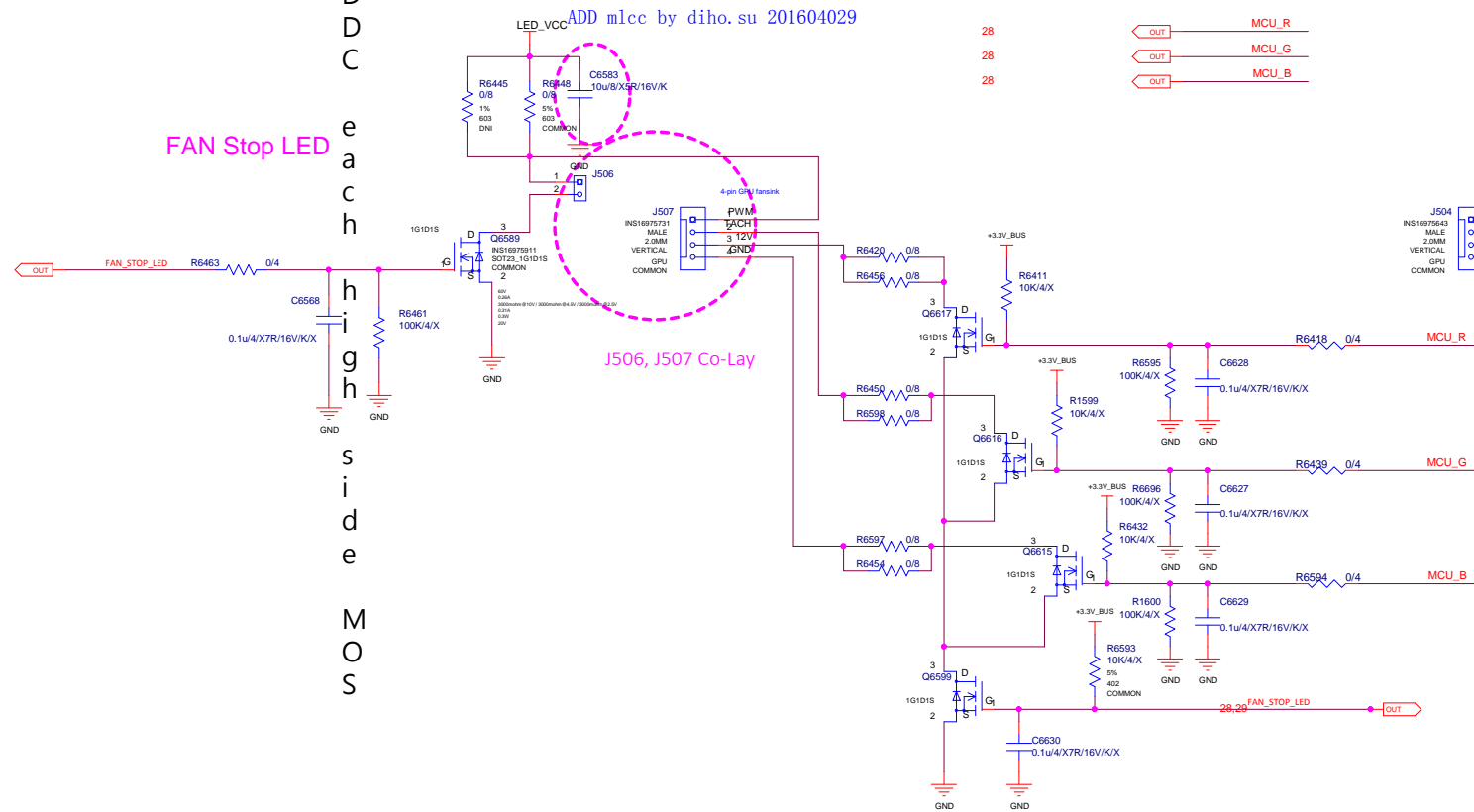


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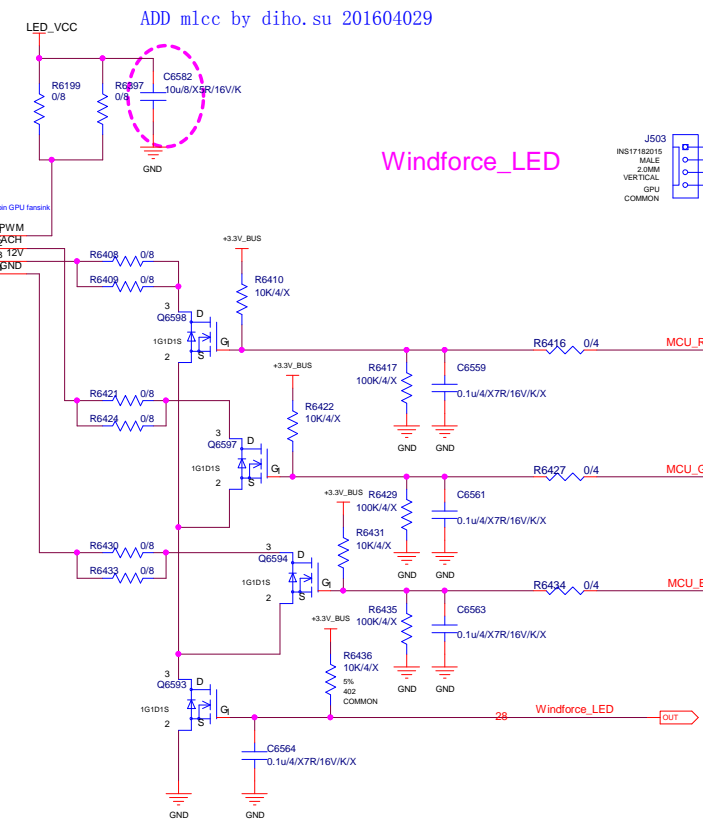
Near VDDC each side MOS

FAN Stop LED

ADD mlcc by diho.su 201604029

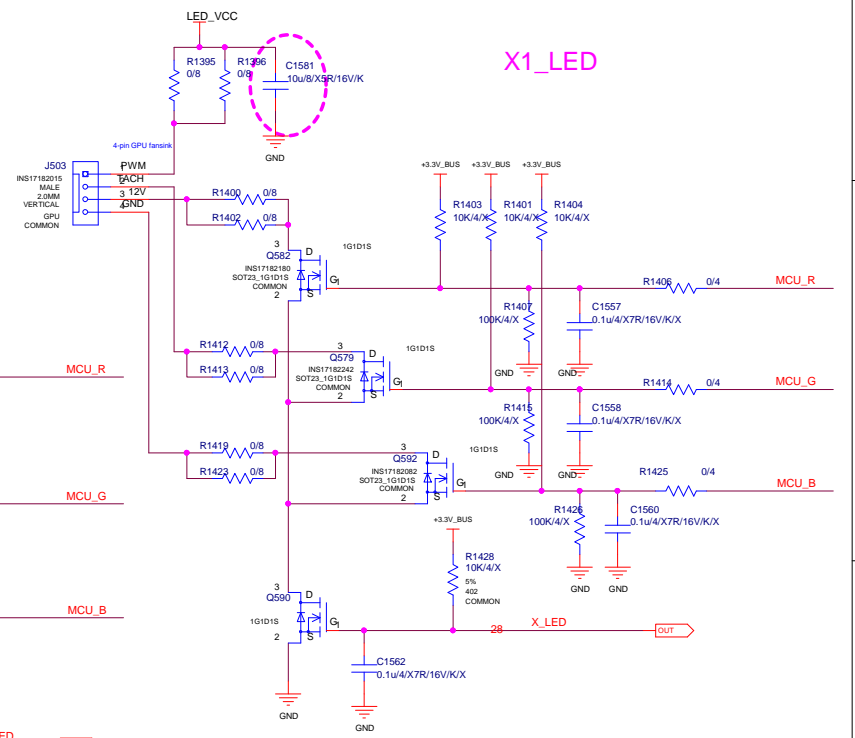


ADD mlcc by diho.su 201604029



Windforce_LED

X1_LED



Title										
Size	Document Number									Rev
Custom	FAN GV-RX570GAMING-8GD-MI H									1.11
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PAGE DETAIL

MECH: Bracket/Thermal